



Ultra Low Capacitance Array for ESD Protection

Description

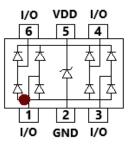
The SSCE5V051SB is a high performance and low-cost design which includes surge rated diode arrays to protect high speed data interfaces. The SSCE5V051SB family has been specifically designed to protected sensitive components. Which are connected to data and transmission lines, from over-voltage caused by Electrostatic Discharging (ESD). Electrical Fast Transients (EFT), and lightning.

The SSCE5V051SB is a unique design which includes surge rated, low capacitance steering diodes and a unique design of clamping cell which is an equivalent TVS diodes in a single package. During transient conditions, the steering diodes direct the transient to either the power supply line or to the ground line. The internal unique design of clamping cell prevents over-voltage on the power line, protecting any downstream components. The SSCE5V051SB may be used to meet the ESD immunity requirements of IEC 61000-4-2, level 4(±30KV air, ±30KV contact discharge).

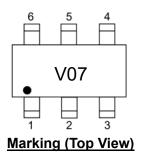
• Feature

- ♦ SOT23-6L Package
- ♦ Working voltage: 5V
- ♦ Low clamping voltage
- ♦ Low capacitance
- ♦ Low leakage current
- ♦ Response Time is<1 ns</p>
- RoHS compliant
- ♦ Complies with following standards:
 IEC 61000-4-2 (ESD) immunity test
 Air discharge: ±30kV
 Contact discharge: ±30kV
 - -IEC 61000-4-5(Surge) 40A (8/20µs)

PIN configuration



Circuit Diagram



• Applications

- \diamond Video Graphics Cards
- $\diamond\,$ USB2.0 Power and Data lines protection
- ♦ Notebook and PC Computers
- ♦ Monitors and Flat Panel Displays
- ♦ IEEE 1394 Firewire Ports
- ♦ SIM Ports

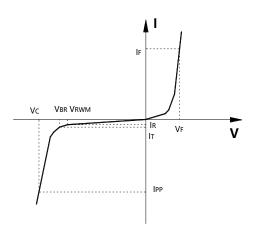
Mechanical data

- ♦ Lead finish:100% matte Sn (Tin)
- ♦ Mounting position: Any
- ♦ Qualified max reflow temperature:260°C
- ♦ Device meets MSL 3 requirements
- ♦ Pure tin plating: 7 ~ 17 um
 Pin flatness: ≤3mil



• Electronic Parameter

Symbol	Parameter		
V _{RWM}	Peak Reverse Working Voltage		
I _R	Reverse Leakage Current @ VRWM		
V _{BR}	Breakdown Voltage @ I⊤		
Ι _Τ	Test Current		
IPP	Maximum Reverse Peak Pulse Current		
Vc	Clamping Voltage @ IPP		
P _{PP}	Peak Pulse Power		
CJ	Junction Capacitance		



• Absolute maximum rating $@T_A = 25^{\circ}C$

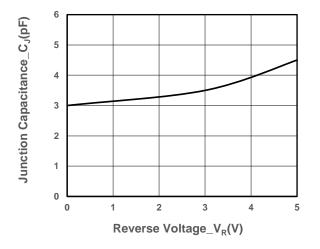
Parameter		Symbol	Value	Unit
Peak Pulse Power (8/20µs)		P _{PP}	720	W
Peak Pulse Current (8/20µs)		IPP	40	А
ESD Rating per IEC61000-4-2:	Contact	\/	30	KV
	Air	Vesd	30	
Storage Temperature		T _{STG}	-55/+150	°C
Operating Temperature		TJ	-55/+125	°C

• Electrical Characteristics @T_A = 25°C

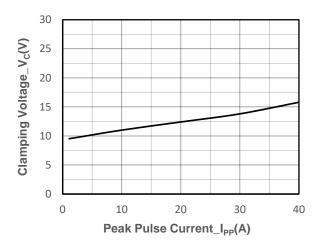
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Peak Reverse Working Voltage	VRWM				5	V
Breakdown Voltage	V_{BR}	I⊤ = 1mA	6.0			V
Reverse Leakage Current	I _R	V _{RWM} = 5V			1	μA
Clamping Voltage	V _{C1}	I _{PP} = 1A, t _P = 8/20μs		9.5	11	V
(I/O pin to GND)	V _{C2}	I _{PP} = 40A, t _P = 8/20μs		16	18	V
Clamping Voltage (VDD pin to GND)	Vc	I _{PP} = 90A, t _P = 8/20µs			19	V
Junction Capacitance	CJ	V _R = 0V, f = 1MHz, between I/O pins			3	pF
Junction Capacitance	CJ	$V_R = 0V$, f = 1MHz, I/O pins pin to GND			5	pF



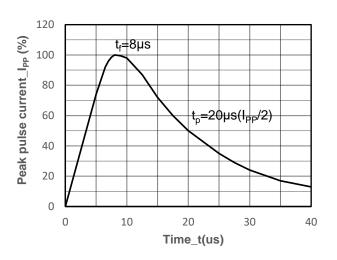
• Typical Performance Characteristics



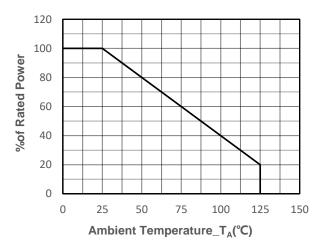
Junction Capacitance vs. Reverse Voltage



Clamping Voltage vs. Peak Pulse Current



8/20µs Pulse Waveform



Power derating vs. Ambient temperature



• Package Information

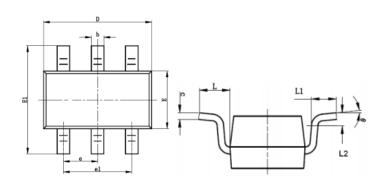
Ordering Information

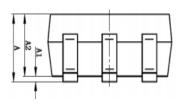
Device	Package	Qty per Reel	Reel Size
SSCE5V051SB	SOT23-6L	3000	7 Inch

Mechanical Data

Case: SOT23-6L

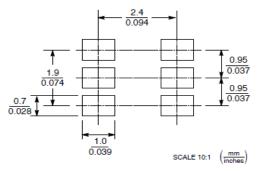
Case Material: Molded Plastic. UL Flammability





DIM	Millimeters				
	Min	Max			
Α	0.900	1.450			
A1	0.000	0.150			
A2	0.900	1.300			
b	0.300	0.500			
С	0.080	0.210			
D	2.720	3.120			
Е	1.400	1.800			
E1	2.600	2.950			
е	0.950BSC				
el	1.9BSC				
L1	0.300	0.600			
L	0.7REF				
L2	0.25REF				
θ	0	8			

Recommended Pad outline (Unit: mm)





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