

SSC8019GN2

P-Channel Enhancement Mode MOSFET

Features

V _{DS}	V _{GS}	R _{DS(ON)} Typ.	l _D
-16V	+12V	15mΩ@-4V5	124
	± 12V	-12A 20mΩ@-2V5	-12A

Description

This device is produced with high cell density DMOS trench technology, which is especially used to minimize on-state resistance. This device particularly suits low voltage applications such as portable equipment, power management and other battery powered circuits, and low in-line power dissipation are needed in a very small outline surface mount package.

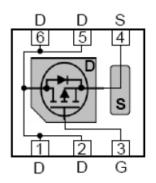
Applications

- Load Switch
- Portable Devices
- DCDC Conversion
- Charging

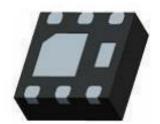
> Ordering Information

Device	Package	Shipping
SSC8019GN2	DFN2020-6L	3000/Reel

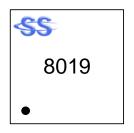
> Pin Configuration



DFN2020-6L (Top View)



Bottom View



Marking



➤ Absolute Maximum Ratings (T_A=25°C unless otherwise noted)

Symbol	Parameter	Ratings	Unit		
V _{DS}	Drain-to-Source Volta	Drain-to-Source Voltage		V	
V _{GS}	Gate-to-Source Volta	Gate-to-Source Voltage		V	
1-	Continuous Drain Current d	T _C =25°C	-12	^	
l _D	Continuous Drain Current	Tc=100°C	-6.6	А	
I _{DM}	Pulsed Drain Curren	-46	Α		
D-	Power Dissipation ^c	Tc=25°C	3.9	10/	
P _D		T _C =100℃	1.6	W	
TJ	Operation junction temperature		-55~150	$^{\circ}$ C	
Tstg	Storage temperature range		-55~150	C	

➤ Thermal Resistance Ratings (T_A=25°C unless otherwise noted)

Symbol	Parameter	Maximum	Unit
R _{θJA}	Junction-to-Ambient Thermal Resistance a	32	°C/W

Note:

- a. The value of $R_{\theta JA}$ is measured with the device mounted on 1 in² FR-4 board with 2oz.copper, in a still air environment with T_A =25 °C. The value in any given application depends on the user is specific board design. The power dissipation is based on the t≤10s thermal resistance rating.
- b. Repetitive rating, pulse width limited by junction temperature.
- c. The power dissipation P_D is based on T_{J(MAX)}=150°C, using junction-to-case thermal resistance, and is more useful in setting the upper dissipation limit for cases where additional heat sinking is used.
- d. The maximum current rating is package limited.

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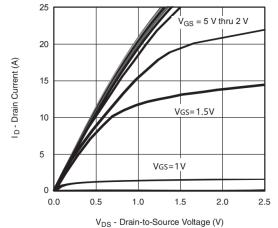


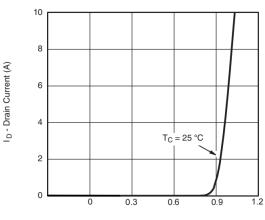
➤ Electrical Characteristics (T_A=25°C unless otherwise noted)

Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit
Drain-Source Breakdown Voltage	V _{(BR)DSS}	V _{GS} = 0V, I _D =- 250uA	-16			V
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_{D} = -250uA$	-0.4	-0.65	-1	V
Drain Course On Registeres	R _{DS(on)}	V _{GS} = -4.5V, I _D = -4.1A		15	25	0
Drain-Source On-Resistance		V _{GS} = -2.5V, I _D = -3A		20	30	mΩ
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = -12V, V _{GS} = 0V			-1	μA
Gate-Source Leak Current	I _{GSS}	V _{GS} = ±12V, V _{DS} = 0V			±100	nA
Transconductance	G _{FS}	V _{DS} = -10V, I _D = -1A		10		s
Forward Voltage	V _{SD}	V _{GS} = 0V, I _S = -1A			-1.3	V
Input Capacitance	Ciss	V - 40V V - 0V		1850		
Output Capacitance	Coss	$V_{DS} = -10V, V_{GS} = 0V,$		190		pF
Reverse Transfer Capacitance	C _{RSS}	f = 1MHz		170		
Total Gate Charge	Q _G	45/// 40//		16		
Gate to Source Charge	Q _{GS}	$V_{GS} = -4.5V, V_{DS} = -10V,$		3		nC
Gate to Drain Charge	Q _{GD}	- I _D =-5A		4		
Turn-on Delay Time	T _{D(ON)}	45/1/ 40/		31		
Rise Time	Tr	$V_{GS} = -4.5V$, $V_{DS} = -10V$,		27		
Turn-off Delay Time	T _{D(OFF)}	$R_L = 6\Omega$, $R_G = 3\Omega$,		125		ns
Fall Time	T _f	- I _D =-1A		83		



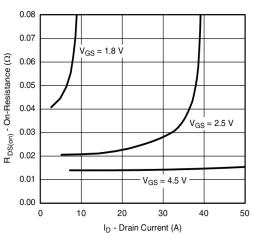
> Typical Performance Characteristics (T_A=25℃ unless otherwise noted)



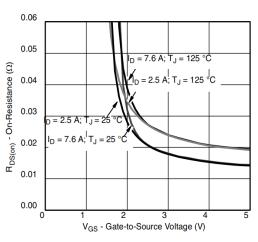


V_{GS} - Gate-to-Source Voltage (V) **Transfer Characteristics**

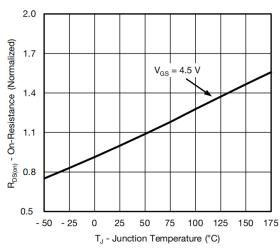




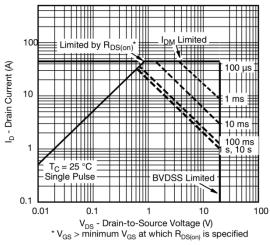
On-Resistance vs. Drain Current



On-Resistance vs. Gate-to-Source Voltage



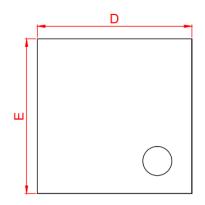
On-Resistance vs. Junction Temperature



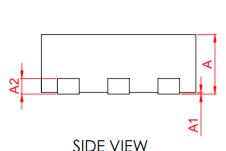
Safe Operating Area

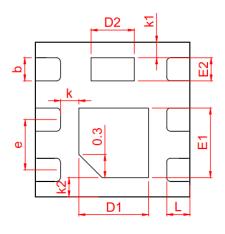


Package Information









BOTTOM VIEW

SYMBOL	MILLIMETER				
STIVIBUL	MIN	NOM	MAX		
Α	0.50	0.55	0.60		
* A1	0.00	0.02	0.05		
★ b	0.25	0.30	0.35		
★ A2	0.152 BSC				
* D	1.95	2.00	2.05		
★ E	1.95	2.00	2.05		
★ E1	0.80	0.90	1.00		
★ E2	0.25	0.30	0.35		
★ D1	0.80	0.90	1.00		
★ D2	0.46	0.56	0.66		
★ e	0.65 REF				
* L	0.25	0.30	0.35		
* K	0.20	0.25	0.30		
★ K1	0.15	0.20	0.25		
★ K2	0.20	0.25	0.30		

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