



## SSC8068GN4

### N-Channel Enhancement Mode MOSFET

#### ➤ Features

V <sub>DS</sub>	V <sub>GS</sub>	R <sub>DS(ON)</sub> Typ.	I <sub>D</sub>
60V	±20V	24mΩ@10V	25A
		28mΩ@4V5	

#### ➤ Description

This SSC8068GN4 uses advanced trench technology to provide excellent RDSON and low gate charge. The complementary MOSFETS may be used to form a level shifted high side switch, and for a host of other applications.

**100% UIS + ΔVDS + Rg Tested!**

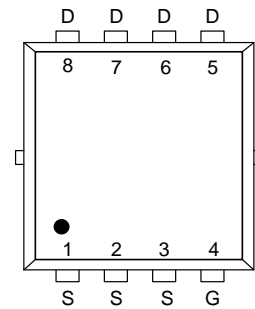
#### ➤ Applications

- Load Switch
- PWM Application
- Power Management
- Motor Driving in Power Tool, E-vehicle, Robotics

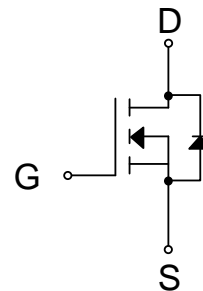
#### ➤ Ordering Information

Device	Package	Shipping
SSC8068GN4	PDFN3.3X3.3-8L	5000/Reel

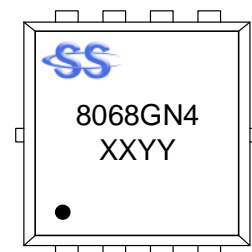
#### ➤ Pin configuration



**PDFN3.3X3.3-8L (Top View)**



**Pin Configuration**



**Marking**

(XXYY: Internal Traceability Code)



### ➤ Absolute Maximum Ratings (T<sub>A</sub>=25°C unless otherwise noted)

Symbol	Parameter	Ratings	Unit
V <sub>DSS</sub>	Drain-to-Source Voltage	60	V
V <sub>GSS</sub>	Gate-to-Source Voltage	±20	V
I <sub>D</sub>	Continuous Drain Current <sup>d</sup>	T <sub>C</sub> =25°C	25
		T <sub>C</sub> =100°C	14
I <sub>DSM</sub>	Continuous Drain Current <sup>a</sup>	T <sub>A</sub> =25°C	9
		T <sub>A</sub> =70°C	6
I <sub>DM</sub>	Pulsed Drain Current <sup>b</sup>	90	A
P <sub>D</sub>	Power Dissipation <sup>c</sup>	T <sub>C</sub> =25°C	27.8
		T <sub>C</sub> =100°C	11.1
P <sub>DSM</sub>	Power Dissipation <sup>a</sup>	T <sub>A</sub> =25°C	3.13
		T <sub>A</sub> =70°C	2
I <sub>AS</sub>	Avalanche Current <sup>b</sup> L=0.5mH Single Pulse	10	A
E <sub>AS</sub>	Avalanche Energy <sup>b</sup> L=0.5mH Single Pulse	25	mJ
T <sub>J</sub>	Operation junction temperature	-55~150	°C
T <sub>STG</sub>	Storage temperature range	-55~150	

### ➤ Thermal Resistance Ratings (T<sub>A</sub>=25°C unless otherwise noted)

Symbol	Parameter	Ratings	Unit
R <sub>θJA</sub>	Junction-to-Ambient Thermal Resistance <sup>a</sup>	40	°C/W
R <sub>θJC</sub>	Junction-to-Case Thermal Resistance	4.5	

Note:

- The value of R<sub>θJA</sub> is measured with the device mounted on 1 in<sup>2</sup> FR-4 board with 2oz.copper, in a still air environment with T<sub>A</sub>=25°C. The value in any given application depends on the user is specific board design. The power dissipation is based on the t≤10s thermal resistance rating.
- Repetitive rating, pulse width limited by junction temperature.
- The power dissipation P<sub>D</sub> is based on T<sub>J(MAX)</sub>=150°C, using junction-to-case thermal resistance, and is more useful in setting the upper dissipation limit for cases where additional heat sinking is used.
- The maximum current rating is package limited.

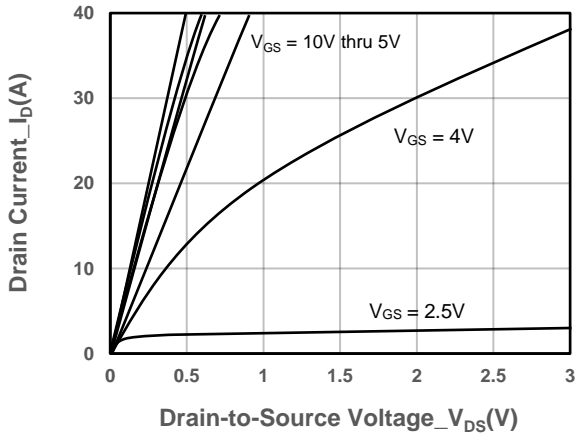


➤ **Electrical Characteristics (T<sub>A</sub>=25°C unless otherwise noted)**

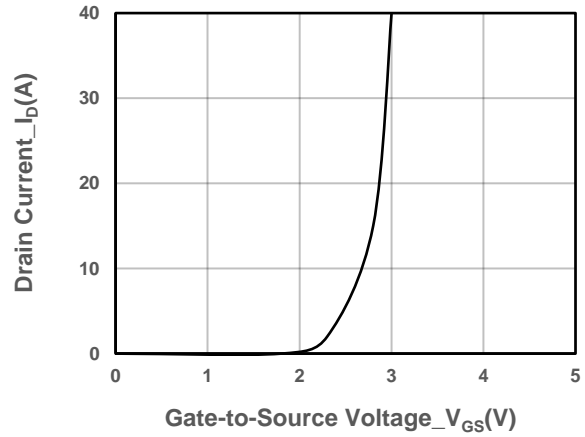
Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
Drain-Source Breakdown Voltage	V <sub>(BR)DSS</sub>	V <sub>GS</sub> = 0V, I <sub>D</sub> = 250μA	60			V
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 250uA	1	1.5	2.5	V
Drain-Source On-Resistance	R <sub>DS(on)</sub>	V <sub>GS</sub> = 10V, I <sub>D</sub> = 6A		24	30	mΩ
		V <sub>GS</sub> = 4.5V, I <sub>D</sub> = 3A		28	40	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> = 60V, V <sub>GS</sub> = 0V			1	μA
Gate-Source Leak Current	I <sub>GSS</sub>	V <sub>GS</sub> = ±20V, V <sub>DS</sub> = 0V			±100	nA
Transconductance	G <sub>FS</sub>	V <sub>DS</sub> = 5V, I <sub>D</sub> = 5A		10		s
Forward Voltage	V <sub>SD</sub>	V <sub>GS</sub> = 0V, I <sub>S</sub> = 5A		0.8	1.3	V
Gate Resistance	R <sub>G</sub>	V <sub>DS</sub> = 0V, f = 1MHz		1.3		Ω
Input Capacitance	C <sub>ISS</sub>	V <sub>DS</sub> = 50V, V <sub>GS</sub> = 0V, f = 1MHz		1400		pF
Output Capacitance	C <sub>OSS</sub>			70		
Reverse Transfer Capacitance	C <sub>RSS</sub>			58		
Total Gate Charge	Q <sub>G</sub>	V <sub>GS</sub> = 10V, V <sub>DS</sub> = 30V, I <sub>D</sub> = 5A		6		nC
Gate to Source Charge	Q <sub>GS</sub>			1.6		
Gate to Drain Charge	Q <sub>GD</sub>			1.3		
Turn-on Delay Time	T <sub>D(ON)</sub>	V <sub>GS</sub> = 10V, V <sub>DS</sub> = 10V, R <sub>L</sub> = 6Ω, R <sub>G</sub> = 6Ω		8		ns
Rise Time	T <sub>r</sub>			58		
Turn-off Delay Time	T <sub>D(OFF)</sub>			20		
Fall Time	T <sub>f</sub>			88		
Diode Recovery Time	T <sub>rr</sub>	I <sub>F</sub> =5A, di/dt=500A/us		12		ns
Diode Recovery Charge	Q <sub>rr</sub>	I <sub>F</sub> =5A, di/dt=500A/us		15		nC



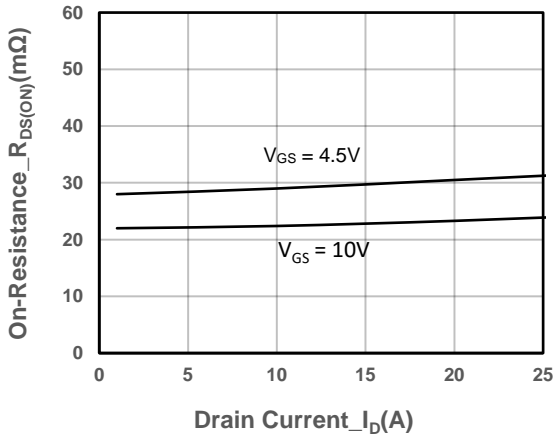
➤ Typical Performance Characteristics (T<sub>A</sub>=25°C unless otherwise noted)



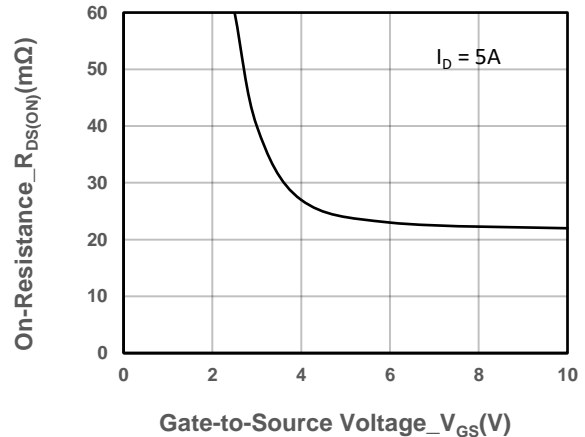
Output Characteristics



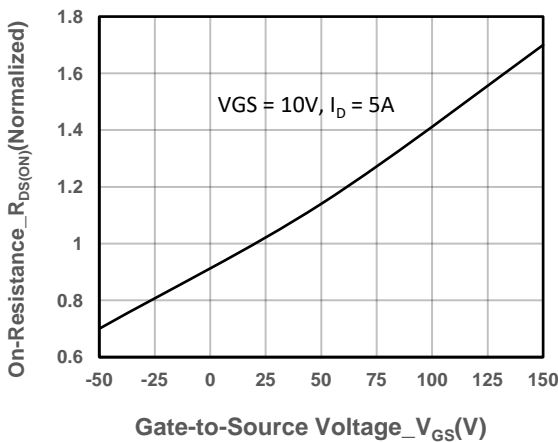
Transfer Characteristics



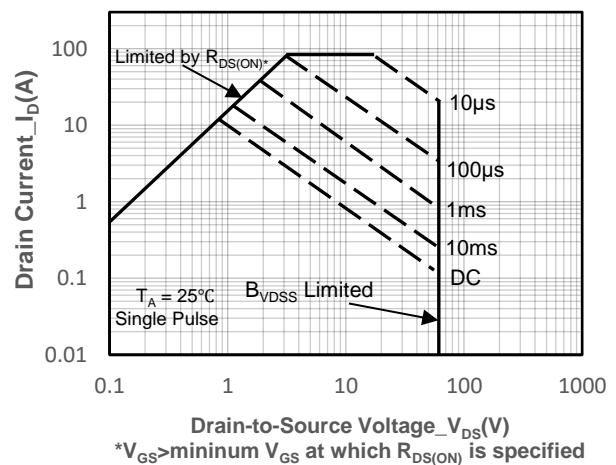
On-Resistance vs. Drain Current and Gate Voltage



On-Resistance vs. Gate-to-Source Voltage

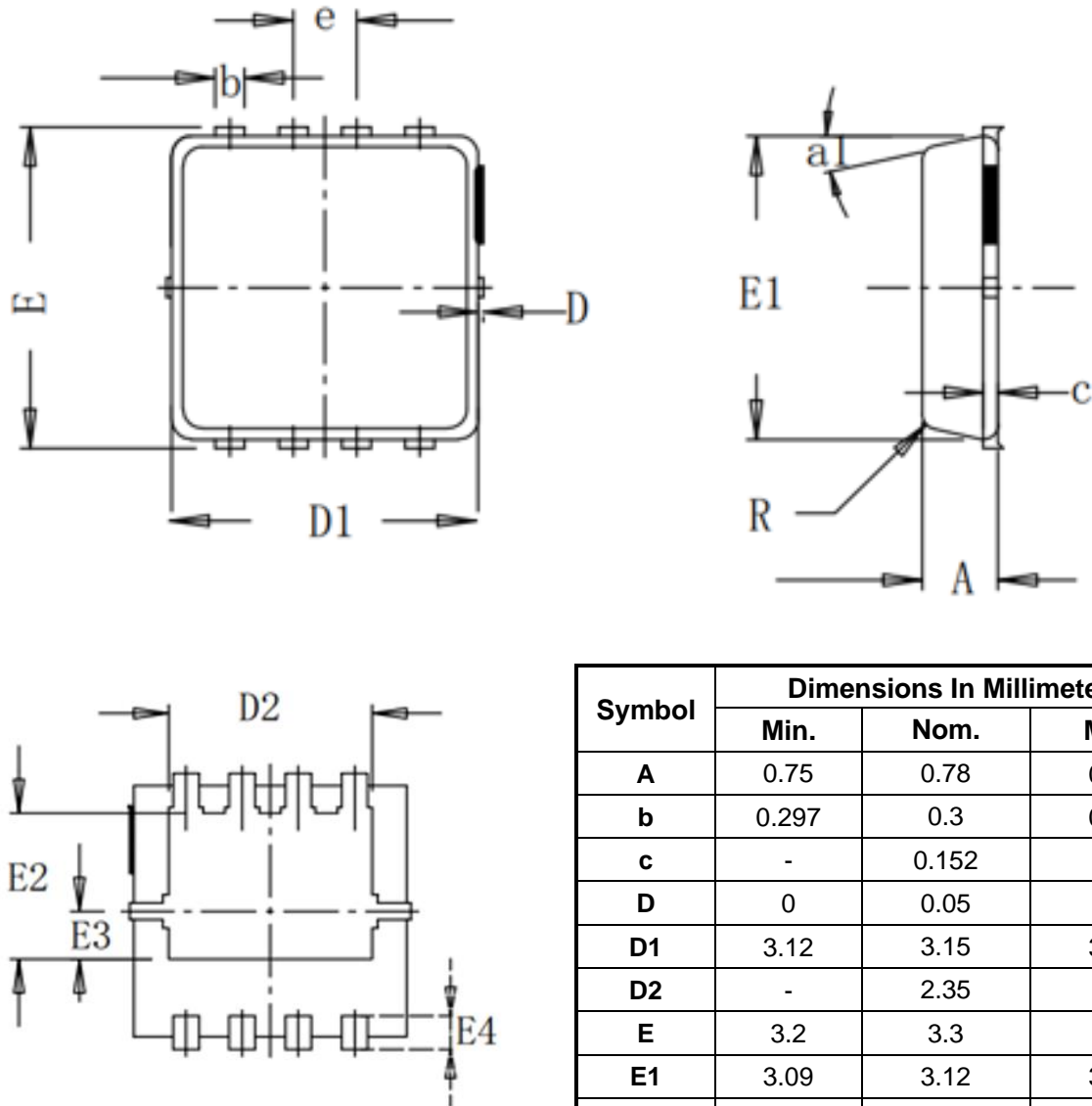


On-Resistance vs. Junction Temperature



Safe Operating Area vs. Junction-to-Ambient

## ➤ Package Information



Symbol	Dimensions In Millimeters		
	Min.	Nom.	Max.
A	0.75	0.78	0.81
b	0.297	0.3	0.35
c	-	0.152	-
D	0	0.05	0.1
D1	3.12	3.15	3.18
D2	-	2.35	-
E	3.2	3.3	3.4
E1	3.09	3.12	3.15
E2	-	1.75	-
E3	-	0.575	-
E4	-	0.4	-
R	-	0.15	-
e	0.65BSC		
a1°	-	12°	-



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