

## **SSC8068GT8**

## N-Channel Enhancement Mode MOSFET

#### Features

V <sub>DS</sub>	V <sub>GS</sub>	R <sub>DS(ON)</sub> Typ.	ID
60V	+20V	23mΩ@10V	30A
	<u> </u>	27mΩ@4V5	304

#### > Description

This device is N-Channel enhancement MOSFET. Uses advanced trench technology and design to provide excellent RDSON with low gate charge. This device is suitable for use in DC-DC conversion, power switch and charging circuit.

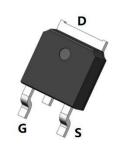
100% UIS + ΔVDS + Rg Tested!

- > Applications
- Motor Drive Control
- Portable Devices
- DCDC Conversion
- Power Supplies
- Synchronous Rectification

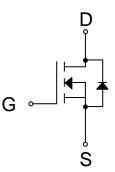
#### > Ordering Information

Device	Package	Shipping
SSC8068GT8	TO-252-2L	2500/Reel

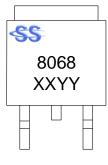
### > Pin Configuration



TO-252-2L (Top View)



Pin Configuration



<u>Marking</u>

(XXYY: Internal Traceability Code)

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Symbol	Parameter	Ratings	Unit		
V <sub>DSS</sub>	Drain-to-Source Volta	60	V		
V <sub>GSS</sub>	Gate-to-Source Volta	Gate-to-Source Voltage			
L_	Continuous Drain Current d	Tc <b>=25</b> ℃	30	^	
lD	Continuous Drain Current <sup>d</sup>	Tc=100℃	18	A	
Idsm	Continuous Drain Current <sup>a</sup>	T <sub>A</sub> =25℃	10		
		T <sub>A</sub> =70℃	7	A	
I <sub>DM</sub>	Pulsed Drain Current	Pulsed Drain Current <sup>b</sup>		Α	
P		Tc <b>=25</b> ℃	46	14/	
PD	Power Dissipation <sup>c</sup>	$T_{c}=25^{\circ}C$ $T_{c}=100^{\circ}C$ $T_{A}=25^{\circ}C$ $T_{A}=70^{\circ}C$ rent b $T_{c}=25^{\circ}C$ $T_{c}=100^{\circ}C$ $T_{A}=25^{\circ}C$ $T_{A}=70^{\circ}C$ nH Single Pulse nH Single Pulse	18	W	
Розм		T <sub>A</sub> =25℃	4.2	W	
	Power Dissipation <sup>a</sup>	T <b></b> , <b>=70</b> ℃	2.7		
las	Avalanche Energy <sup>b</sup> L=0.5mH Single Pulse		13	А	
Eas	Avalanche Energy <sup>b</sup> L=0.5mH Single Pulse		42.25	mJ	
TJ	Operation junction temperature		-55~150	°C	
Tstg	Storage temperature range		-55~150	°C	

#### > Absolute Maximum Ratings ( $T_A=25^{\circ}C$ unless otherwise noted)

#### ➤ Thermal Resistance Ratings (T<sub>A</sub>=25°C unless otherwise noted)

Symbol	Parameter	Ratings	Unit
Reja	Junction-to-Ambient Thermal Resistance <sup>a</sup>	30	°C/W
R <sub>θJC</sub>	Junction-to-Case Thermal Resistance	2.7	C/ <b>V</b>

Note:

- a. The value of R<sub>θJA</sub> is measured with the device mounted on 1 in<sup>2</sup> FR-4 board with 2oz.copper, in a still air environment with T<sub>A</sub>=25 °C.The value in any given application depends on the user is specific board design. The power dissipation is based on the t≤10s thermal resistance rating.
- b. Repetitive rating, pulse width limited by junction temperature.
- c. The power dissipation P<sub>D</sub> is based on T<sub>J(MAX)</sub>=150°C, using junction-to-case thermal resistance, and is more useful in setting the upper dissipation limit for cases where additional heat sinking is used.
- d. The maximum current rating is package limited.

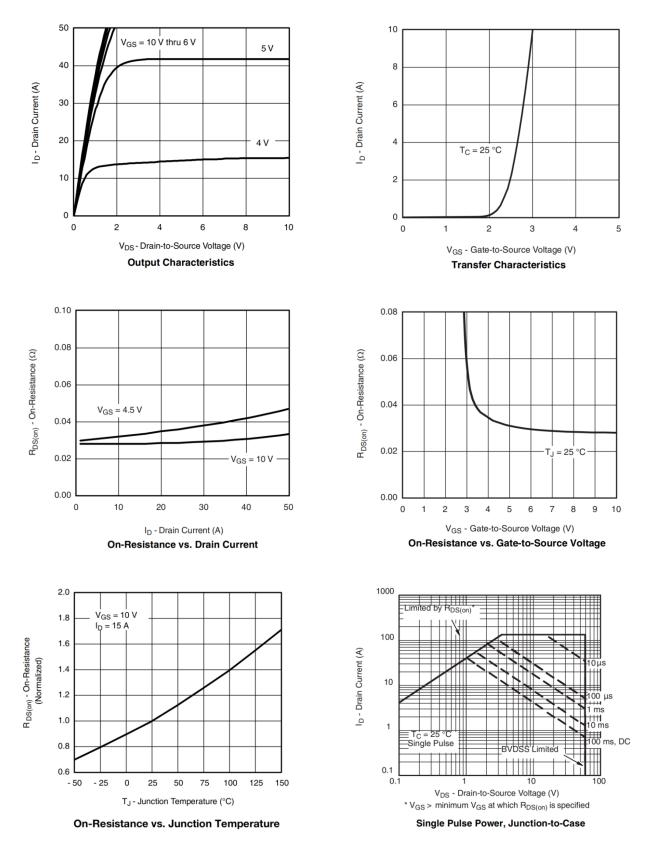


# > Electrical Characteristics (T<sub>A</sub>=25 $^{\circ}$ C unless otherwise noted)

Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit
Drain-Source Breakdown Voltage	V(BR)DSS	V <sub>GS</sub> = 0V, I <sub>D</sub> = 250µA	60			V
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}$ , $I_D = 250 uA$	1	1.5	2.5	V
		V <sub>GS</sub> = 10V, I <sub>D</sub> = 6A		23	31	mΩ
Drain-Source On-Resistance	R <sub>DS(on)</sub>	V <sub>GS</sub> = 4.5V, I <sub>D</sub> = 3A		27	40	mΩ
Zero Gate Voltage Drain Current	IDSS	V <sub>DS</sub> = 60V, V <sub>GS</sub> = 0V			1	μA
Gate-Source Leak Current	lgss	$V_{GS}$ = ±20V, $V_{DS}$ = 0V			±100	nA
Forward Voltage	V <sub>SD</sub>	V <sub>GS</sub> = 0V, I <sub>S</sub> = 5A		0.8	1.3	V
Gate Resistance	RG	V <sub>DS</sub> = 0V, f = 1MHz		1.5		Ω
Input Capacitance	Ciss	$\lambda = 25 \lambda \lambda = 0 \lambda$		1320		
Output Capacitance	Coss	$V_{DS} = 25V, V_{GS} = 0V,$ f = 1MHz		68		pF
Reverse Transfer Capacitance	C <sub>RSS</sub>			59		
Total Gate Charge	Q <sub>G</sub>			24		
Gate to Source Charge	Q <sub>GS</sub>	V <sub>GS</sub> = 10V, V <sub>DS</sub> = 30V, I <sub>D</sub> = 15A		4.4		nC
Gate to Drain Charge	Q <sub>GD</sub>			6.3		
Turn-on Delay Time	T <sub>D(ON)</sub>			7.3		
Rise Time	Tr	V <sub>GS</sub> = 10V, V <sub>DS</sub> = 30V,		20		
Turn-off Delay Time	T <sub>D(OFF)</sub>	I <sub>D</sub> = 15A, R <sub>G</sub> = 1.8Ω		15		ns
Fall Time	T <sub>f</sub>	]		22		
Diode Recovery Time	Trr	I <sub>F</sub> =30A, di/dt=100A/us		28		ns
Diode Recovery Charge	Qrr	I <sub>F</sub> =30A, di/dt=100A/us		45		nC



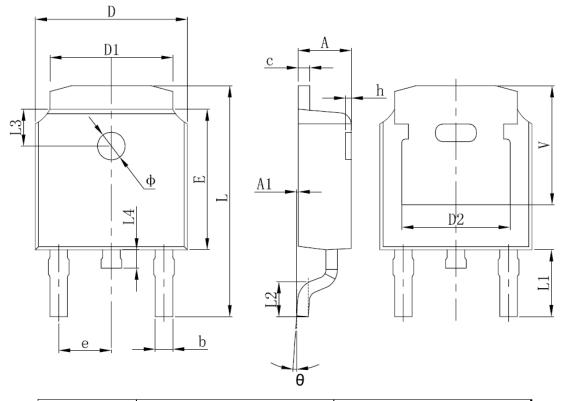
## > Typical Performance Characteristics (T<sub>A</sub>=25℃ unless otherwise noted)



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## > Package Information



Symbol	Dimensions In Millimeters		Dimensions In Inches		
	Min.	Max.	Min.	Max.	
A	2.200	2.400	0.087	0.094	
A1	0.000	0.127	0.000	0.005	
b	0.635	0.770	0.025	0.030	
с	0.460	0.580	0.018	0.023	
D	6.500	6.700	0.256	0.264	
D1	5.100	5.460	0.201	0.215	
D2	4.830	REF.	0.190 REF.		
E	6.000	6.200	0.236	0.244	
e	2.186	2.386	0.086	0.094	
L	9.712	10.312	0.382	0.406	
L1	2.900	REF.	0.114 REF.		
L2	1.400	1.700	0.055	0.067	
L3	1.600 REF.		0.063 REF.		
L4	0.600	1.000	0.024	0.039	
Φ	1.100	1.300	0.043	0.051	
θ	0°	<mark>8</mark> °	0°	8°	
h	0.000	0.300	0.000	0.012	
V	5.250 REF.		0.207 REF.		



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