

SSC80314GN6

N-Channel Enhancement Mode MOSFET

> Features

V _{DS}	V _{GS}	R _{DS(ON)} Typ.	ID
	+ 2014	4.3mΩ@10V	95 4
30V	±20V	6.9mΩ@4.5V	85A

> Description

This device uses advanced trench technology to provide excellent RDSON and low gate charge. The complementary MOSFETS may be used to form a level shifted high side switch, and for a host of other applications.

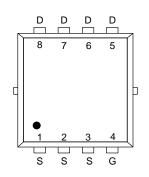
100% UIS + ΔVDS + Rg Tested!

- > Applications
- Load Switch
- NB/PC
- DCDC Conversion
- Motor Drive

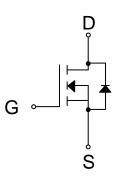
> Ordering Information

Device	Package	Shipping	
SSC80314GN6	PDFN5X6-8L	5000/Reel	

Pin Configuration



PDFN5X6-8L (Top View)



Pin Configuration



Marking

(XXYY: Internal Traceability Code)







Symbol	Parameter	Ratings	Unit	
V _{DSS}	Drain-to-Source Voltage		30	V
V _{GSS}	Gate-to-Source Volta	Gate-to-Source Voltage		V
	$I_D \qquad \qquad \text{Continuous Drain Current} \stackrel{d}{=} \frac{T_C = 25^{\circ}C}{T_C = 100^{\circ}C}$	Tc =25 ℃	85	
ID		Tc=100℃	48	A
Idsm	Continuous Drain Current ^a	T _A =25℃	19	•
		T _A =70℃	14	A
Ідм	Pulsed Drain Curren	320	Α	
_		Tc=25℃	62	W
PD	Power Dissipation ^c	T _c =100°C	25	
Pdsm	Power Dissipation ^a	T _A =25℃	2.8	W
		T _A =70℃	1.8	
Eas	Avalanche Energy ^b L=0.5mH	100	mJ	
TJ	Operation junction temperature		-55~150	°C
Tstg	Storage temperature range		-55~150	°C

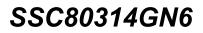
> Absolute Maximum Ratings ($T_A=25^{\circ}$ unless otherwise noted)

> Thermal Resistance Ratings ($T_A=25^{\circ}C$ unless otherwise noted)

Symbol	Parameter	Maximum	Unit
Reja	Junction-to-Ambient Thermal Resistance ^a	44	℃/W
R _{θJC}	Junction-to-Case Thermal Resistance	2	C/VV

Note:

- a. The value of R_{θJA} is measured with the device mounted on 1 in² FR-4 board with 2oz.copper, in a still air environment with T_A=25[°]C.The value in any given application depends on the user is specific board design. The power dissipation is based on the t≤10s thermal resistance rating.
- b. Repetitive rating, pulse width limited by junction temperature.
- c. The power dissipation P_D is based on T_{J(MAX)}=150°C, using junction-to-case thermal resistance, and is more useful in setting the upper dissipation limit for cases where additional heat sinking is used.
- d. The maximum current rating is package limited.



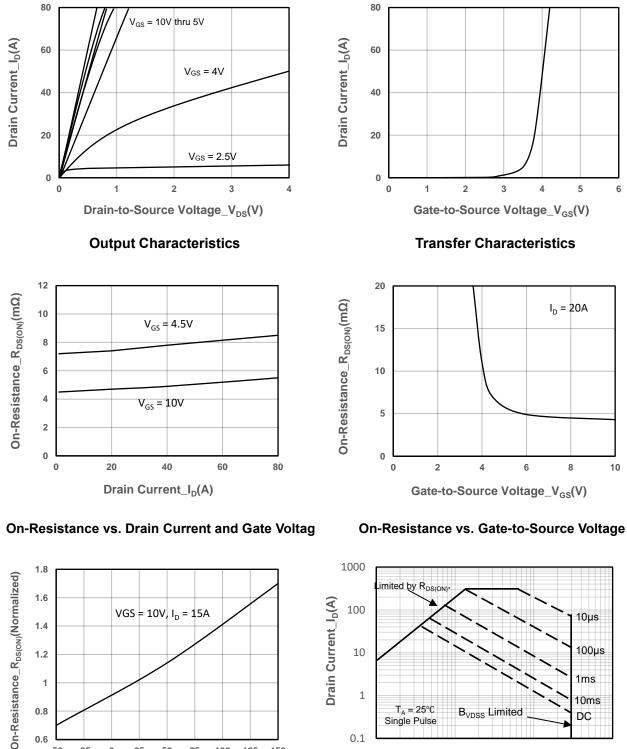


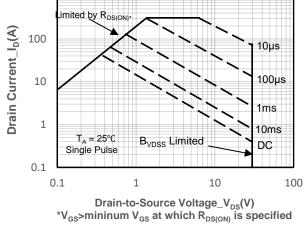
\succ Electrical Characteristics (T_A=25 °C unless otherwise noted)

Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit
Drain-Source Breakdown Voltage	V _(BR) dss	V _{GS} = 0V, I _D = 250µA	30			V
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = 250 \mu A$	1	1.5	2.5	V
Drain-Source On-Resistance	$R_{DS(on)}$	V _{GS} = 10V, I _D = 30A		4.3	5.6	m0
		V_{GS} = 4.5V, I _D = 20A		6.9	9	- mΩ
Zero Gate Voltage Drain Current	IDSS	V _{DS} = 30V, V _{GS} = 0V			1	μA
Gate-Source Leak Current	lgss	V_{GS} = ±20V, V_{DS} = 0V			±100	nA
Transconductance	G _{FS}	V _{DS} = 5V, I _D = 10A		26		s
Forward Voltage	Vsd	V _{GS} = 0V, I _S = 1A		0.75	1.3	V
Gate Resistance	Rg	f=1MHz		2.5		Ω
Input Capacitance	Ciss			1500		
Output Capacitance	Coss	$V_{DS} = 15V, V_{GS} = 0V,$		540		pF
Reverse Transfer Capacitance	Crss	f = 1MHz		120		
Total Gate Charge	Q_{G}			18.1		
Gate to Source Charge	Q _{GS}	V _{GS} = 10V, V _{DS} = 15V, I _D =10A		3.4		nC
Gate to Drain Charge	Q _{GD}	ID – IOA		3.1		
Turn-on Delay Time	T _{D(ON)}			8		
Rise Time	Tr	$V_{GS} = 10V, V_{DS} = 15V,$ $R_{L} = 15\Omega, R_{G} = 3\Omega,$		2.9		
Turn-off Delay Time	T _{D(OFF)}			19		ns
Fall Time	T _f	- I _D =1A		5.6		
Diode Recovery Time	Trr	I⊧=20A, di/dt=100A/us		15		ns
Diode Recovery Charge	Q _{rr}	I _F =20A, di/dt=100A/us		8		nC



Typical Performance Characteristics (T_A=25℃ unless otherwise noted) \triangleright





Safe Operating Area vs. Junction-to-Ambient

1.4

1.2

1

0.8

0.6 -50

-25

0

25

50

Gate-to-Source Voltage_V_{GS}(V)

On-Resistance vs. Junction Temperature

75

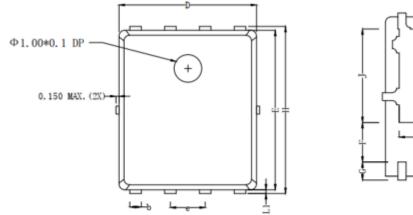
100 125 150

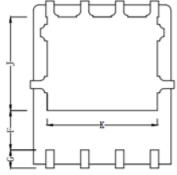
VGS = 10V, I_D = 15A

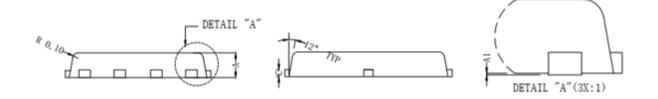




> Package Information







Symbol	Dimensions In Millimeters			
Symbol	Min.	Nom.	Max.	
A	0.90	1.00	1.10	
A1	0.00	0.03	0.05	
b	0.25	0.03	0.35	
С	0.254 REF			
D	4.80	4.90	5.00	
F	1.35 REF			
E	5.65	5.75	5.85	
е	1.27 BSC			
Н	5.90	6.00	6.10	
L1	0.10	0.13	0.16	
G	0.55 REF			
к	4.00 REF			
J	3.45 REF			





DISCLAIMER

SSCSEMI RESERVES THE RIGHT TO MAKE CHANGES WITHOUT FURTHER NOTICE TO ANY PRODUCTS HEREIN TO IMPROVE RELIABILITY, FUNCTION OR DESIGN. SSCSEMI DOES NOT ASSUME ANY LIABILITY ARISING OUT OF THE APPLICATION OR USE OF ANY PRODUCT OR CIRCUIT DESCRIBED HEREIN; NEITHER DOES IT CONVEY ANY LICIENCE UNDER ITS PATENT RIGHTS, NOR THE RIGHTS OF OTHERS.

THE GRAPHS PROVIDED IN THIS DOCUMENT ARE STATISTICAL SUMMARIES BASED ON A LIMITED NUMBER OF SAMPLES AND ARE PROVIDED FOR INFORMATIONAL PURPOSE ONLY. THE PERFORMANCE CHARACTERISTICS LISTED IN THEM ARE NOT TESTED OR GUARANTEED. IN SOME GRAPHS, THE DATA PRESENTED MAY BE OUTSIDE THE SPECIFIED OPERATING RANGE (E.G. OUTSIDE SPECIFIED POWER SUPPLY RANGE) AND THEREFORE OUTSIDE THE WARRANTED RANGE.

OUR PRODUCT SPECIFICATIONS ARE ONLY VALID IF OBTAINED THROUGH THE COMPANY'S OFFICIAL WEBSITE, CRM SYSTEM, OR OUR SALES PERSONNEL CHANNELS. IF CHANGES OR SPECIAL VERSIONS ARE INVOLVED, THEY MUST BE STAMPED WITH A QUALITY SEAL AND MARKED WITH A SPECIAL VERSION NUMBER TO BE VALID.