

SSC80312GT8

N-Channel Enhancement Mode MOSFET

> Features

V _{DS}	V _{GS}	R _{DS(ON)} Typ.	ID
30V	1.201/	3.4mΩ@10V	110A
	±20V	4.9mΩ@4V5	HUA

> Description

This device is N-Channel enhancement MOSFET.

Uses advanced trench technology and design to provide excellent RDSON with low gate charge. This device is suitable for use in DC-DC conversion, power switch and charging circuit.

100% UIS + ΔVDS + Rg Tested!

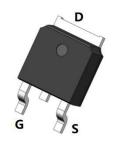
Applications

- Motor Drive Control
- Portable Devices
- DCDC Conversion
- Power Supplies
- Synchronous Rectification

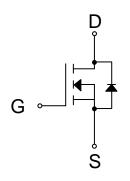
Ordering Information

Device	Package	Shipping
SSC80312GT8	TO-252-2L	2500/Reel

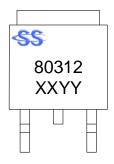
Pin Configuration



TO-252-2L (Top View)



Pin Configuration



Marking

(XXYY: Internal Traceability Code)



➤ Absolute Maximum Ratings (T_A=25°C unless otherwise noted)

Symbol	Parameter	Ratings	Unit	
V_{DSS}	Drain-to-Source Voltage		30	V
V _{GSS}	Gate-to-Source Volta	ge	±20	V
,	Continuous Dunin Cumout d	T _C =25℃	110	
I _D	Continuous Drain Current	T _C =100℃	62	A
	Continuos Dunin Comment 2	T _A =25℃	27	Δ.
lоsм	Continuous Drain Current ^a	T _A =70°C	20	A
I _{DM}	Pulsed Drain Curren	t ^b	420	Α
D.		Tc=25°C	73.5	10/
P _D	Power Dissipation ^c	$\begin{array}{c} \text{Gate-to-Source Voltage} \\ \text{S Drain Current} ^{d} & & & & & & \\ & & & & & & & & \\ & & & & & & & & \\ & & & & & & & \\ & & & & & & & \\ & & & & & & & \\ & & & & & & \\ & & & & & & \\ & & & & & & \\ & & & & & & \\ & & & & & & \\ & & & & & & \\ & & & & & \\ & & & & & & \\ & & & & & & \\ & & & & & \\ & & & & & \\ & & & & & \\ & & & & & \\ & & & & & \\ & & & & & \\ & & & & & \\ & & & & & \\ & & & & & \\ & & & & & \\ & & & & & \\ & & & & & \\ & & & & & \\ & & & & & \\ & & & & & \\ & & & & \\ & & & & & \\ & & & & \\ & & & & \\ & & & & & \\ & & & \\ & & & & \\ & & & & \\ & & & \\ & & & & \\ & & & \\ & & & & \\ &$	29.4	W
5	D Distriction 6	T _A =25℃	4.2	10/
P _{DSM}	Power Dissipation ^a	T _A =70°C	2.7	W
las	Avalanche Energy ^b L=0.5mH \$	25	Α	
Eas	Avalanche Energy ^b L=0.5mH Single Pulse		156	mJ
TJ	Operation junction temperature		-55~150	°C
T _{STG}	Storage temperature ra	-55~150	$^{\circ}$	

➤ Thermal Resistance Ratings (T_A=25°C unless otherwise noted)

Symbol	Parameter	Ratings	Unit
RθJA	Junction-to-Ambient Thermal Resistance a	30	°C/W
$R_{ heta JC}$	Junction-to-Case Thermal Resistance	1.7	C/VV

Note:

- a. The value of R_{θJA} is measured with the device mounted on 1 in² FR-4 board with 2oz.copper, in a still air environment with T_A=25℃. The value in any given application depends on the user is specific board design. The power dissipation is based on the t≤10s thermal resistance rating.
- b. Repetitive rating, pulse width limited by junction temperature.
- c. The power dissipation P_D is based on $T_{J(MAX)}$ =150°C, using junction-to-case thermal resistance, and is more useful in setting the upper dissipation limit for cases where additional heat sinking is used.
- d. The maximum current rating is package limited.

SSC-V1.1 www.sscsemi.com Analog Future

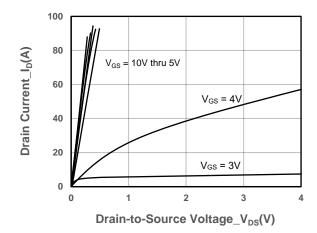


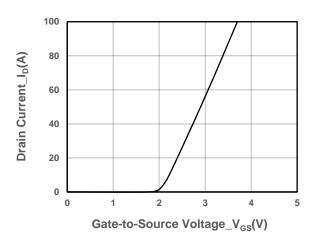
\succ Electrical Characteristics (T_A=25°C unless otherwise noted)

Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit
Drain-Source Breakdown Voltage	V _{(BR)DSS}	$V_{GS} = 0V, I_D = 250\mu A$	30			V
Gate Threshold Voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = 250uA	1	1.6	2.5	V
Drain-Source On-Resistance	Б	V _{GS} = 10V, I _D = 30A		3.4	4.5	0
Drain-Source On-Resistance	R _{DS(on)}	V _{GS} = 4.5V, I _D = 20A		4.9 7.5		mΩ
Zero Gate Voltage Drain Current	IDSS	V _{DS} = 30V, V _{GS} = 0V			1	μA
Gate-Source Leak Current	I _{GSS}	V _{GS} = ±20V, V _{DS} = 0V			±100	nA
Forward Voltage	V _{SD}	V _{GS} = 0V, I _S = 20A		0.8	1.3	V
Gate Resistance	R _G	V _{DS} = 0V, f = 1MHz		2.5		Ω
Input Capacitance	Cıss	\\ -45\\\\ -0\\		2250		
Output Capacitance	Coss	$V_{DS} = 15V$, $V_{GS} = 0V$, $f = 1MHz$		290		pF
Reverse Transfer Capacitance	Crss	T - TIVIDZ		230		
Total Gate Charge	Q _G	\\ -40\\\\ -45\\		41		
Gate to Source Charge	Q _{GS}	$V_{GS} = 10V, V_{DS} = 15V,$		9		nC
Gate to Drain Charge	Q_GD	I _D = 30A		10		
Turn-on Delay Time	T _{D(ON)}			9		
Rise Time	Tr	V _{GS} = 10V, V _{DS} = 15V,		14		
Turn-off Delay Time	T _{D(OFF)}	$I_D = 30A, R_G = 3\Omega$		35		ns
Fall Time	T _f			11		
Diode Recovery Time	Trr	I _F =20A, di/dt=100A/us		12		ns
Diode Recovery Charge	Qrr	I⊧=20A, di/dt=100A/us		2.6		nC



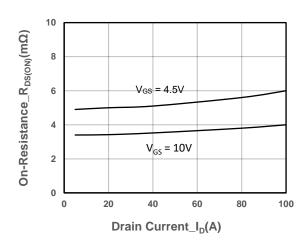
➤ Typical Performance Characteristics (T_A=25°C unless otherwise noted)

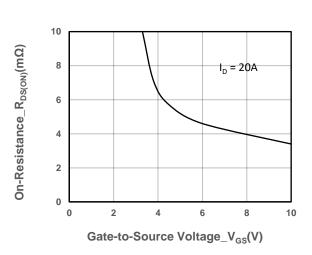




Output Characteristics

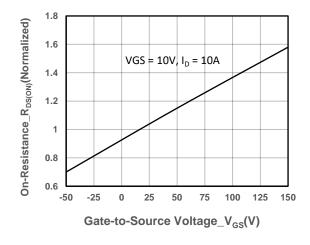
Transfer Characteristics

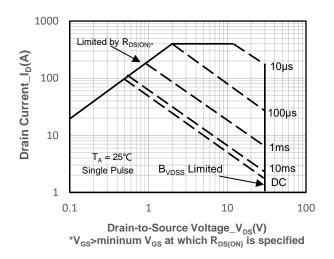




On-Resistance vs. Drain Current and Gate Voltag

On-Resistance vs. Gate-to-Source Voltage



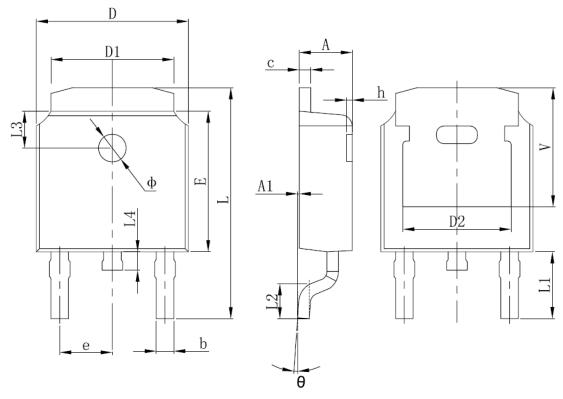


On-Resistance vs. Junction Temperature

Safe Operating Area vs. Junction-to-Ambient



Package Information



Symbol	Dimensions In Millimeters		Dimensions In Inches		
Symbol	Min.	Max.	Min.	Max.	
Α	2.200	2.400	0.087	0.094	
A1	0.000	0.127	0.000	0.005	
b	0.635	0.770	0.025	0.030	
С	0.460	0.580	0.018	0.023	
D	6.500	6.700	0.256	0.264	
D1	5.100	5.460	0.201	0.215	
D2	4.830 REF.		0.190 REF.		
E	6.000	6.200	0.236	0.244	
е	2.186	2.386	0.086	0.094	
L	9.712	10.312	0.382	0.406	
L1	2.900	REF.	REF. 0.114 REF.		
L2	1.400	1.700	0.055	0.067	
L3	1.600 REF.		0.063 REF.		
L4	0.600	1.000	0.024	0.039	
Ф	1.100	1.300	0.043	0.051	
θ	0°	8°	0°	8°	
h	0.000	0.300	0.000	0.012	
V	5.250	REF.	0.207 REF.		



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