



SSC8033GS1

P-Channel Enhancement Mode MOSFET

➤ Features

VDS	VGS	RDSON Typ.	ID
-30V	±20V	41mR@-10V	-6A
		56mR@-4V5	

➤ Description

This device is produced with high cell density, DMOS trench technology, which is especially used to minimize on-state resistance. This device is particularly suited for low voltage power management requiring a wide range of given voltage ratings(4.5V-25V) such as load switch and battery protection.

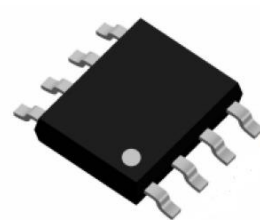
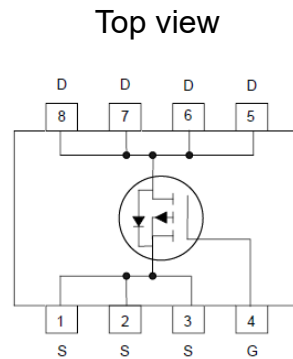
➤ Applications

- Load Switch
- TFT panel power switch
- DCDC conversion

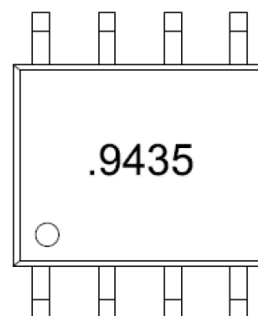
➤ Ordering Information

Device	Package	Shipping
SSC8033GS1	SOP8	4000/Reel

➤ Pin configuration



SOP8



Marking



➤ **Absolute Maximum Ratings**($T_A=25^{\circ}\text{C}$ unless otherwise noted)

Symbol	Parameter	Ratings	Unit
V_{DSS}	Drain-to-Source Voltage	-30	V
V_{GSS}	Gate-to-Source Voltage	± 20	V
I_D	Continuous Drain Current ^a	-6	A
I_{DM}	Pulsed Drain Current ^b	-25	A
P_D	Power Dissipation ^c	2.4	W
P_{DSM}	Power Dissipation ^a	1.3	W
T_J	Operation junction temperature	-55 to 150	$^{\circ}\text{C}$
T_{STG}	Storage temperature range	-55 to 150	$^{\circ}\text{C}$

➤ **Thermal Resistance Ratings**($T_A=25^{\circ}\text{C}$ unless otherwise noted)

Symbol	Parameter	Typical	Maximum	Unit
$R_{\theta JA}$	Junction-to-Ambient Thermal Resistance ^a		100	$^{\circ}\text{C}/\text{W}$
$R_{\theta JC}$	Junction-to-Case Thermal Resistance		55	

Note:

- The value of $R_{\theta JA}$ is measured with the device mounted on 1 in² FR-4 board with 2oz.copper,in a still air environment with $T_A=25^{\circ}\text{C}$.The value in any given application depends on the user is specific board design. The current rating is based on the $t \leq 10\text{s}$ thermal resistance rating.
- Repetitive rating, pulse width limited by junction temperature.
- The power dissipation P_D is based on $T_{J(MAX)}=150^{\circ}\text{C}$, using junction-to-case thermal resistance, and is more useful in setting the upper dissipation limit for cases where additional heat sinking is used.

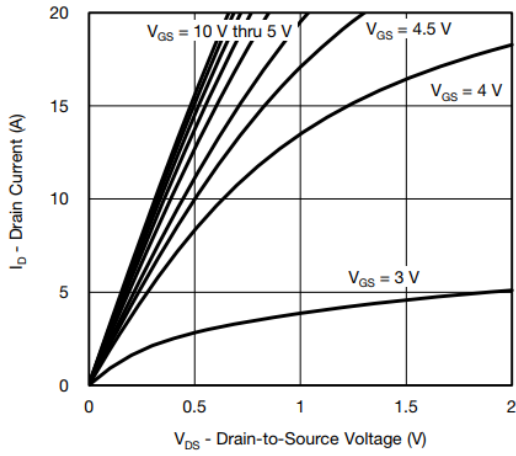


➤ **Electronics Characteristics**($T_A=25^{\circ}\text{C}$ unless otherwise noted)

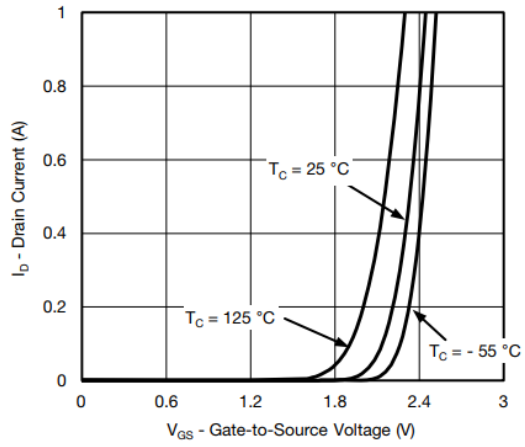
Symbol	Parameter	Test Conditions	Min	Typ.	Max	Unit
$V_{(BR)DSS}$	Drain-Source Breakdown Voltage	$V_{GS}=0V, I_D=-250\mu A$	-30			V
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS}=V_{GS}, I_D=-250\mu A$	-1	-1.5	-3	V
$R_{DS(on)}$	Drain-Source On- Resistance	$V_{GS}=-10V, I_D=-4.5A$		41	60	mR
		$V_{GS}=-4.5V, I_D=-2A$		56	96	
I_{DSS}	Zero Gate Voltage Drain Current	$V_{DS}=-30V, V_{GS}=0V$			-1	μA
I_{GSS}	Gate-Source leak current	$V_{GS}=\pm 20V, V_{DS}=0V$			± 100	nA
G_{FS}	Transconductance	$V_{DS}=-5V, I_D=-6A$		12		S
V_{SD}	Forward Voltage	$V_{GS}=0V, I_S=-1A$		-0.8	-1.6	V
C_{iss}	Input Capacitance	$V_{DS}=-15V, V_{GS}=0V,$ $f=1MHz$		550		pF
C_{oss}	Output Capacitance			60		
C_{rss}	Reverse Transfer Capacitance			50		
$T_{D(ON)}$	Turn-on delay time	$V_{GS}=-10V,$ $V_{DS}=-15V, R_L=2.5R,$ $R_G=3R$		8.6		ns
T_r	Rise time			7.4		
$T_{D(OFF)}$	Turn-off delay time			28.2		
T_f	Fall time			16		



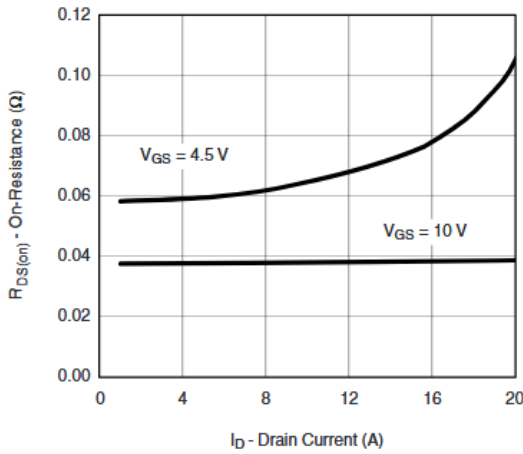
➤ **Typical Characteristics** ($T_A=25^\circ\text{C}$ unless otherwise noted)



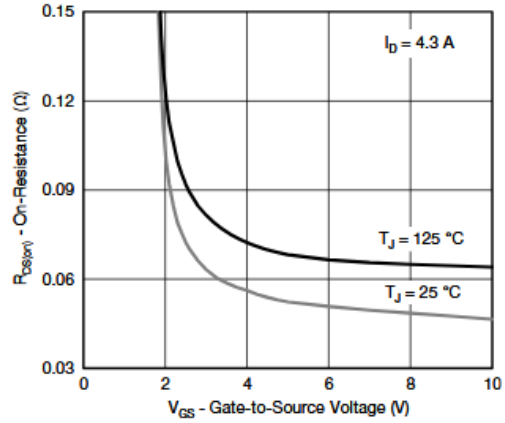
Output Characteristics



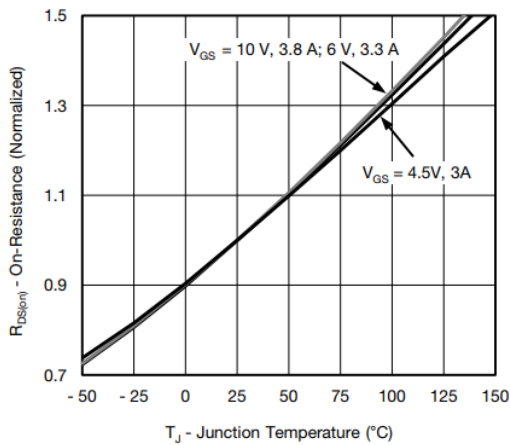
Transfer Characteristics



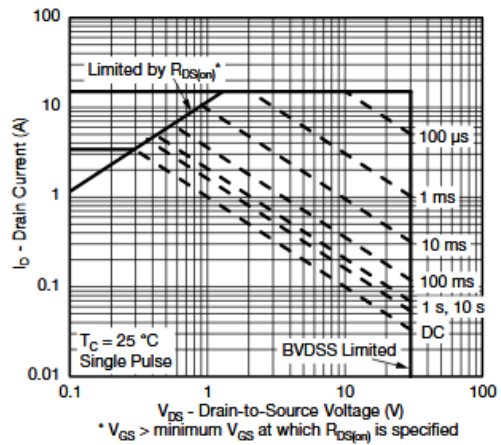
On-Resistance vs. Drain Current



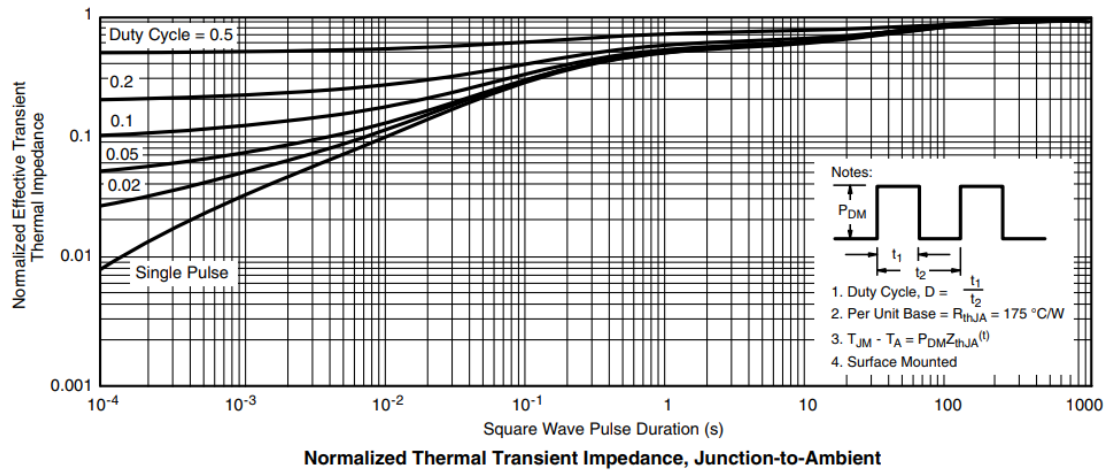
On-Resistance vs. Gate-to-Source Voltage



On-Resistance vs. Junction Temperature

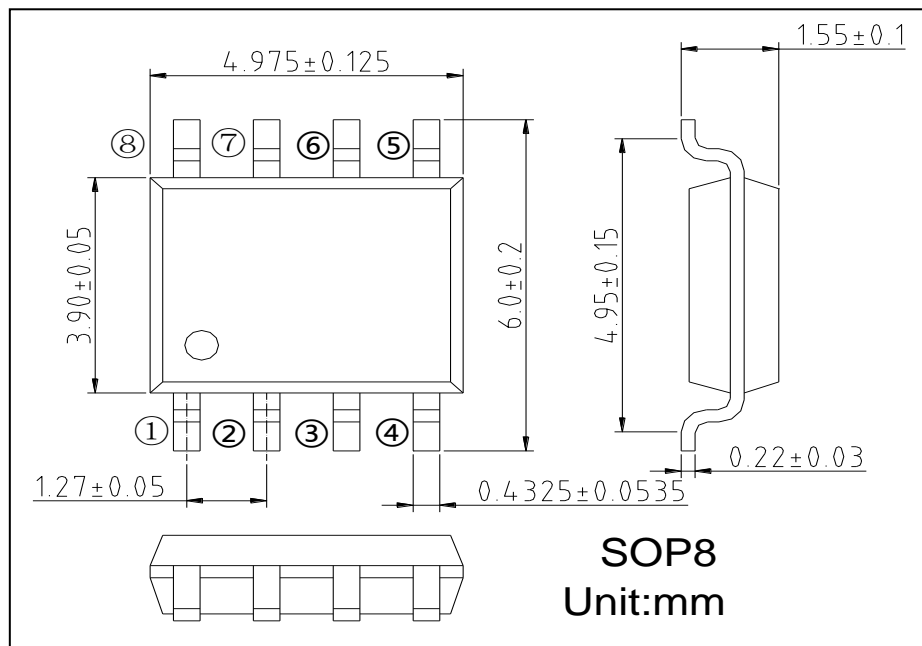


Safe Operating Area, Junction-to-Ambient





➤ Package Information



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