

SSC8022GS6

N-Channel Enhancement Mode MOSFET

> Features

VDS	VGS	RDSON Typ.	ID
20V	±12V	35mR@4V5	2.54
200	±12V	45mR@2V5	3.5A

> Description

This device is produced with high cell density DMOS trench technology, which is especially used to minimize on-state resistance. This device particularly suits low voltage applications such as portable equipment, power management and other battery powered circuits, and low in-line power dissipation are needed in a very small outline surface mount package. Excellent thermal and electrical capabilities.

Applications

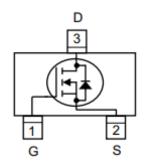
- Load Switch
- Portable Devices
- DCDC conversion

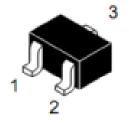
Ordering Information

Device	Package	Shipping
SSC8022GS6	SOT-23	3000/Reel

Pin configuration

Top view





SOT-23



Marking



➤ Absolute Maximum Ratings(T_A=25°C unless otherwise noted)

Symbol	Parameter	Ratings	Unit	
V_{DSS}	Drain-to-Source Voltage	20	V	
V _{GSS}	Gate-to-Source Voltage	±12	V	
I _D	Continuous Drain Current ^a	3.5	Α	
I _{DM}	Pulsed Drain Current b	10	Α	
P_D	Power Dissipation °	0.9	W	
P _{DSM}	Power Dissipation ^a	0.5	W	
TJ	Operation junction temperature	Operation junction temperature -55 to 150		
T _{STG}	Storage temperature range	-55 to 150	°C	

➤ Thermal Resistance Ratings(T_A=25°C unless otherwise noted)

Symbol	Parameter	Typical	Maximum	Unit
$R_{\theta JA}$	Junction-to-Ambient Thermal Resistance ^a		260	°C /\
Rejc	Junction-to-Case Thermal Resistance		150	°C/W

Note:

- a. The value of $R_{\theta JA}$ is measured with the device mounted on 1 in² FR-4 board with 2oz.copper,in a still air environment with T_A =25°C. The value in any given application depends on the user is specific board design. The current rating is based on the t \leq 10s thermal resistance rating.
- b. Repetitive rating, pulse width limited by junction temperature.
- c. The power dissipation P_D is based on $T_{J(MAX)}$ =150°C, using junction-to-case thermal resistance, and is more useful in setting the upper dissipation limit for cases where additional heat sinking is used.

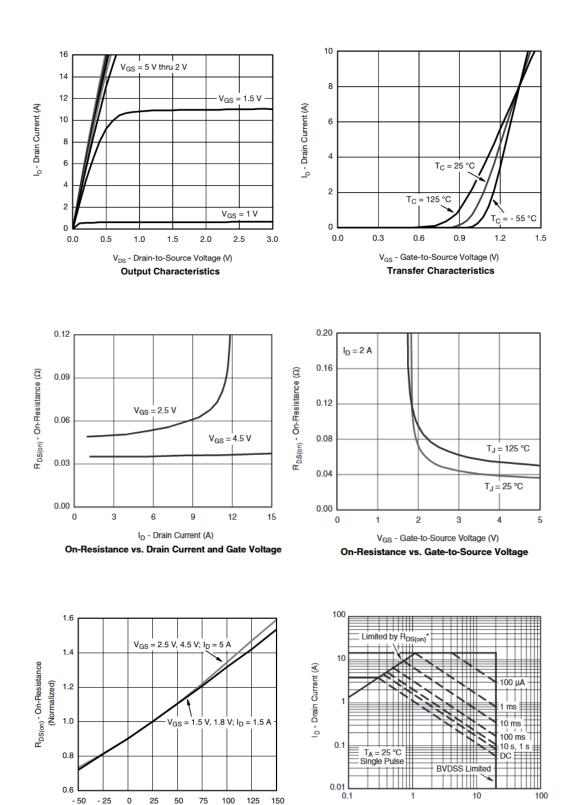


➤ Electronics Characteristics(T_A=25°C unless otherwise noted)

Symbol	Parameter	Test Conditions	Min	Тур.	Max	Unit	
V _{(BR)DSS}	Drain-Source Breakdown Voltage	VGS=0V,ID=250uA	20			>	
V _{GS (th)}	Gate Threshold Voltage	VDS=VGS,ID=250uA	0.4	0.7	1.2	V	
	Drain-Source On-	VGS=4.5V,ID=3.5A		35	50	mR	
R _{DS(on)}	Resistance	VGS=2.5V,ID=3A		45	65		
I _{DSS}	Zero Gate Voltage Drain Current	VDS=20V,VGS=0V			1	uA	
I _{GSS}	Gate-Source leak	VGS=±12V,VDS=0V			±100	nA	
G _{FS}	Transconductance	VDS=5V,ID=3.5A		8	13	S	
V _{SD}	Forward Voltage	VGS=0V,IS=1.1A		0.8	1.15	V	
Ciss	Input Capacitance			450			
Coss	Output Capacitance	VDS=10V, VGS=0V, f=1MHz		70		pF	
Crss	Reverse Transfer Capacitance			43			
T _{D(ON)}	Turn-on delay time			6			
Tr	Rise Time	VGS=4.5V,		9			
T _{D(OFF)}	Turn-off delay time	VDS=5V, RG=6R,ID=3.5A		18		ns	
Tf	Fall Time			12			
Qg	Total Gate charge			11			
Qgs	Gate to Source charge	VGS=4.5V, VDS=10V, ID=3A		1.1		nC	
Qgd	Gate to Drain charge			3.3			



➤ Typical Characteristics(T_A=25°C unless otherwise noted)



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T_J - Junction Temperature (°C)

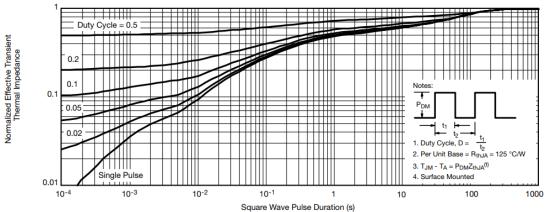
Normalized On-Resistance vs. Junction Temperature

V_{DS} - Drain-to-Source Voltage (V)

* V_{GS} > minimum V_{GS} at which R_{DS(on)} is specified

Safe Operating Area, Junction-to-Ambient

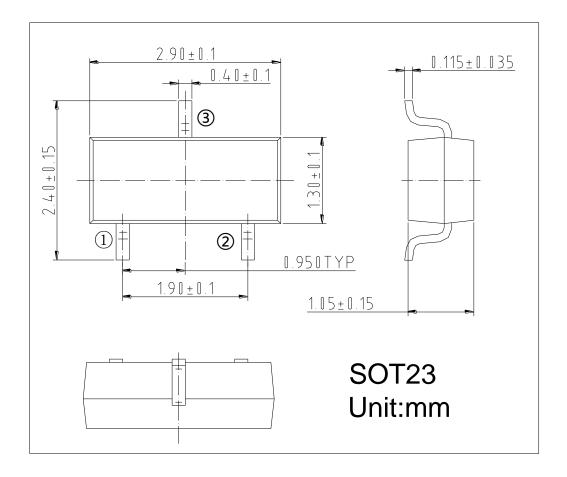




Normalized Thermal Transient Impedance, Junction-to-Ambient



Package Information





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