SSC8034GS6

N-Channel Enhancement Mode MOSFET

➢ Features

VDS	VGS	RDSON Typ.	ID
30V	±12V	18mR@10V	7A
		20mR@4V5	
		30mR@2V5	

➢ Description

This device uses advanced trench technology to provide excellent RDSON and low gate charge and operation with gate voltages as low as 2.5V. This device is suitable for use as a load switch or in PWM applications.

➢ Applications

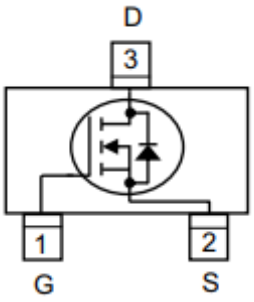
- Load Switch
- Portable Devices
- DCDC conversion

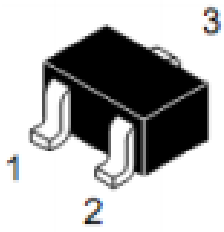
➢ Ordering Information

Device	Package	Shipping
SSC8034GS6	SOT23	3000/Reel

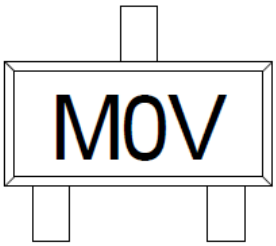
➢ Pin configuration

Top view





SOT23



Marking

➢ Absolute Maximum Ratings(T_A=25℃ unless otherwise noted)

Symbol	Parameter	Ratings	Unit
V_{DSS}	Drain-to-Source Voltage	30	V
V_{GSS}	Gate-to-Source Voltage	±12	V
I_D	Continuous Drain Current [a]	7	A
I_{DM}	Pulsed Drain Current [b]	30	A
P_D	Power Dissipation [c]	1.7	W
P_{DSM}	Power Dissipation [a]	0.85	W
T_J	Operation junction temperature	-55 to 150	℃
T_{STG}	Storage temperature range	-55 to 150	℃

➢ Thermal Resistance Ratings(T_A=25℃ unless otherwise noted)

Symbol	Parameter	Typical	Maximum	Unit
$R_{\theta JA}$	Junction-to-Ambient Thermal Resistance [a]		155	℃/W
$R_{\theta JC}$	Junction-to-Case Thermal Resistance		80	

Note:

a. The value of $R_{\theta JA}$ is measured with the device mounted on 1 in^2 FR-4 board with 2oz.copper,in a still air environment with T_A=25℃.The value in any given application depends on the user is specific board design. The current rating is based on the t≤ 10s thermal resistance rating.

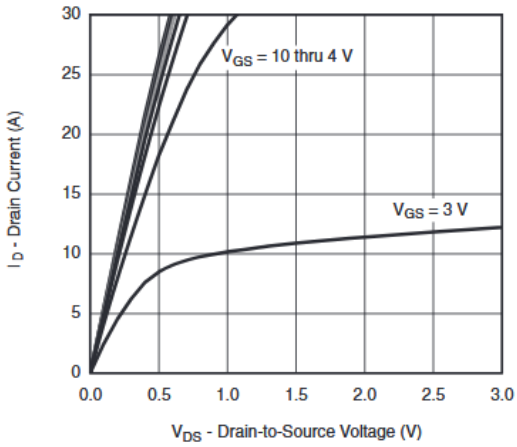
b. Repetitive rating, pulse width limited by junction temperature.

c. The power dissipation P_D is based on $T_{J(MAX)}$=150°C, using junction-to-case thermal resistance, and is more useful in setting the upper dissipation limit for cases where additional heat sinking is used.

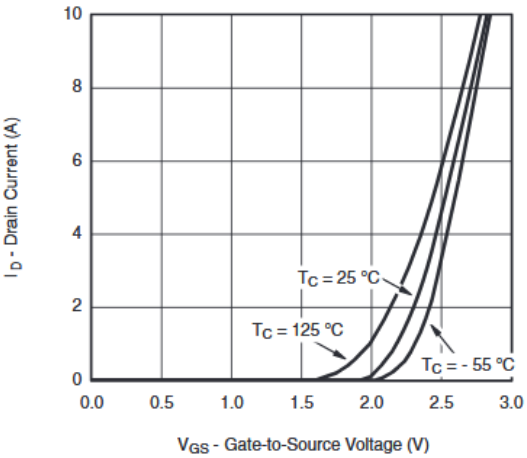
➢ Electronics Characteristics(T_A=25℃ unless otherwise noted)

Symbol	Parameter	Test Conditions	Min	Typ.	Max	Unit
$V_{(BR)DSS}$	Drain-Source Breakdown Voltage	VGS=0V,ID=250uA	30			V
$V_{GS\ (th)}$	Gate Threshold Voltage	VDS=VGS,ID=250uA	0.6	0.8	1.2	V
$R_{DS(on)}$	Drain-Source On-Resistance	VGS=10V,ID=5.8A		18	25	mR
		VGS=4.5V,ID=5A		20	30	
		VGS=2.5V,ID=4A		30	50	
I_{DSS}	Zero Gate Voltage Drain Current	VDS=24V,VGS=0V			1	uA
I_{GSS}	Gate-Source leak current	VGS=±12V,VDS=0V			±100	nA
G_{FS}	Transconductance	VDS=5V,ID=5A		10		S
V_{SD}	Forward Voltage	VGS=0V,IS=1.1A		0.7	1.5	V
Ciss	Input Capacitance	VDS=10V, VGS=0V, f=1MHz		464		pF
Coss	Output Capacitance			78		
Crss	Reverse Transfer Capacitance			64		
$T_{D(ON)}$	Turn-on delay time	VGS=10V, VDS=15V, RG=3R,RL=2.3R		18		ns
Tr	Rise Time			9		
$T_{D(OFF)}$	Turn-off delay time			70		
Tf	Fall Time			22		
Qg	Total Gate charge	VGS=4.5V, VDS=10V, ID=4A		9		nC
Qgs	Gate to Source charge			2		
Qgd	Gate to Drain charge			2.3		

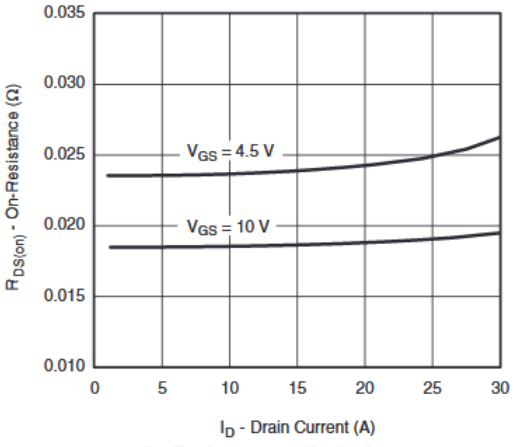
➢ Typical Characteristics(T_A=25℃ unless otherwise noted)



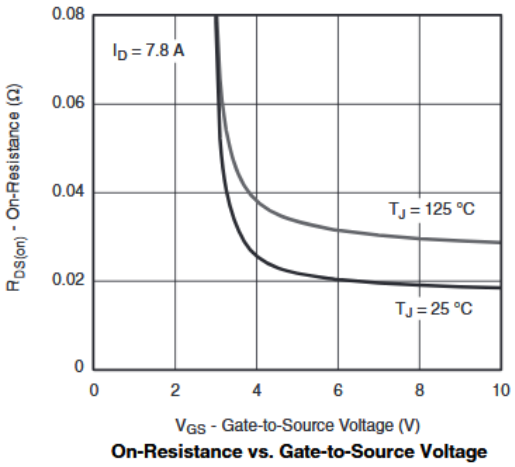
Output Characteristics



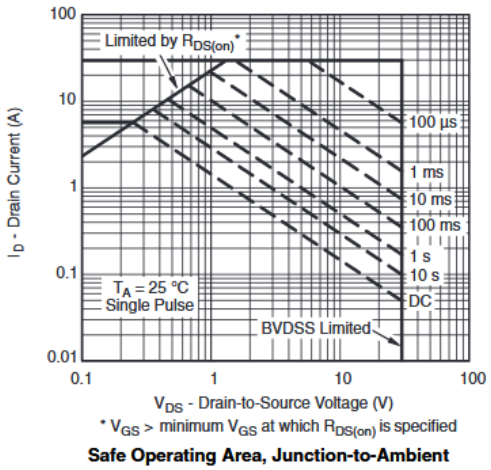
Transfer Characteristics



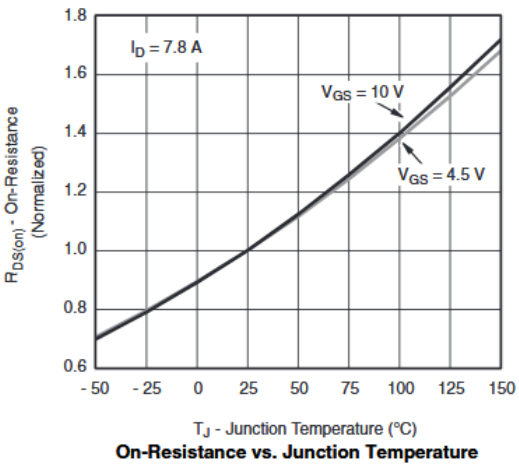
On-Resistance vs. Drain Current



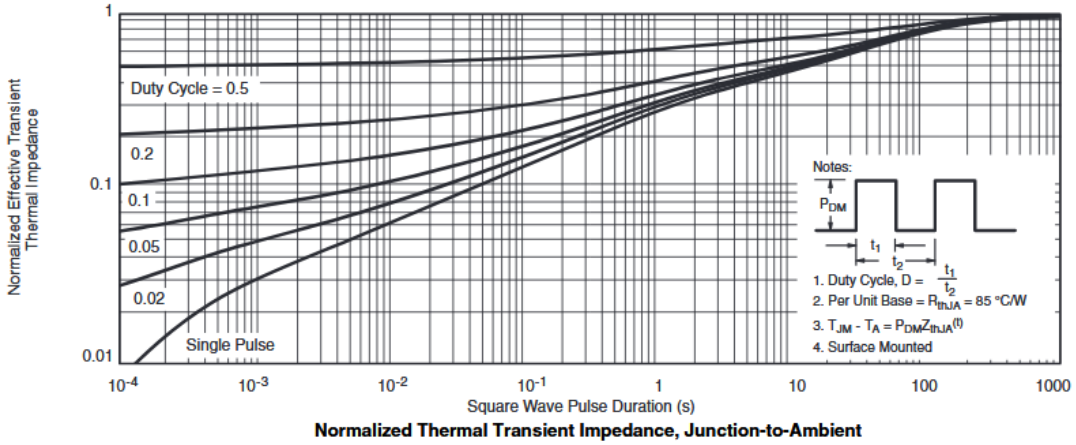
On-Resistance vs. Gate-to-Source Voltage



Safe Operating Area, Junction-to-Ambient

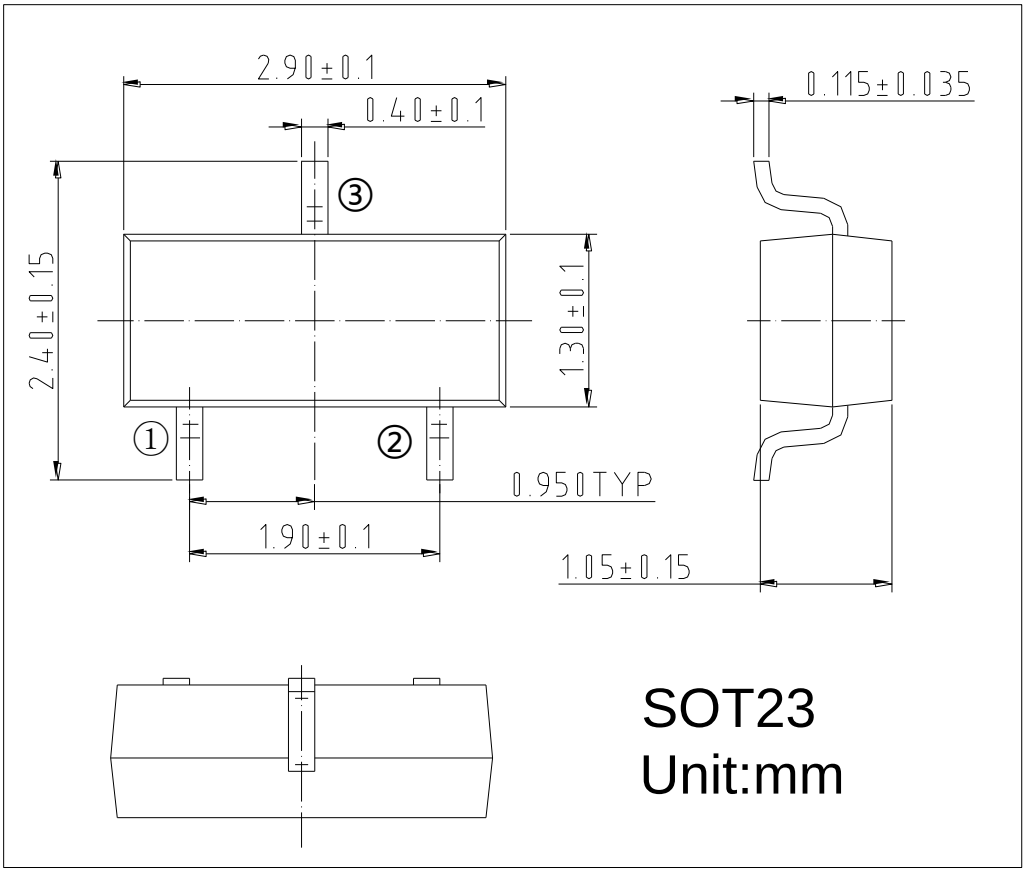


On-Resistance vs. Junction Temperature



Normalized Thermal Transient Impedance, Junction-to-Ambient

➢ Package Information



➢ History Version

V2.0	Product datasheet	2020-07-21
V2.1	Update $V_{GS\ (th)}$ Max limit value	2022-05-24
V2.2	Update Ciss/Coss/Crss type value	2023-04-23

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