SSC8428GS6

N-Channel Enhancement Mode MOSFET

➢ Features

V_{DS}	V_{GS}	$R_{DS(ON)}$ Typ.	I_D
20V	±12V	14mΩ@10V	7A
		16mΩ@4V5	
		20mΩ@2V5	

➢ Description

This SSC8428GS6 combines advanced trench MOSFET technology with a low resistance package to provide extremely low RDSON. This device is ideal for load switch and battery protection applications.

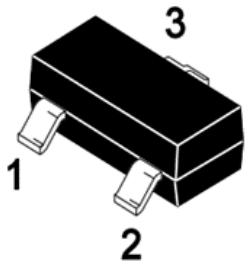
➢ Applications

- Load Switch
- Battery Isolation

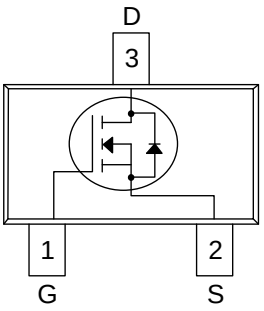
➢ Ordering Information

Device	Package	Shipping
SSC8428GS6	SOT-23	3000/Reel

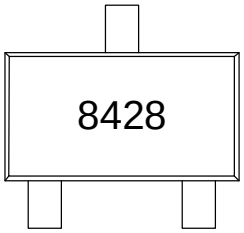
➢ Pin configuration



SOT-23



Pin Configuration (Top View)



Marking

➢ Absolute Maximum Ratings (T_A=25℃ unless otherwise noted)

Symbol	Parameter		Ratings	Unit
V_{DSS}	Drain-to-Source Voltage		20	V
V_{GSS}	Gate-to-Source Voltage		±12	V
I_D	Continuous Drain Current [a]		7	A
I_{DM}	Pulsed Drain Current [b]		30	A
P_D	Power Dissipation [c]	T_C = 25℃	1.2	W
P_{DSM}	Power Dissipation [a]	T_A = 25℃	0.65	W
T_J	Operation junction temperature		-55~150	℃
T_{STG}	Storage temperature range		-55~150	℃

➢ Thermal Resistance Ratings (T_A=25℃ unless otherwise noted)

Symbol	Parameter	Maximum	Unit
$R_{\theta JA}$	Junction-to-Ambient Thermal Resistance [a]	200	℃/W
$R_{\theta JC}$	Junction-to-Case Thermal Resistance	105	

Note:

a. The value of $R_{\theta JA}$ is measured with the device mounted on 1 in^2 FR-4 board with 2oz.copper, in a still air environment with T_A=25℃.The value in any given application depends on the user is specific board design. The power dissipation is based on the t≤10s thermal resistance rating.

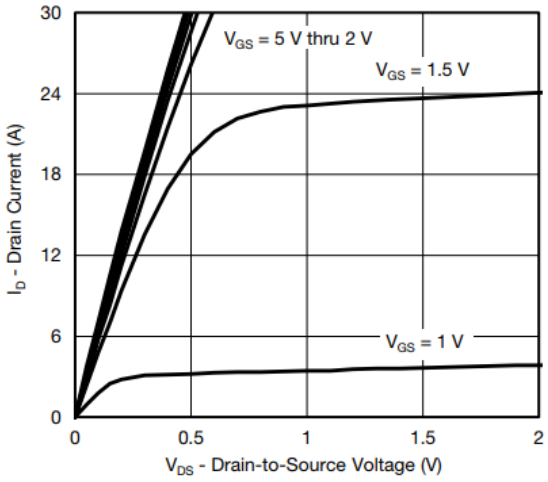
b. Repetitive rating, pulse width limited by junction temperature.

c. The power dissipation P_D is based on $T_{J(MAX)}$=150°C, using junction-to-case thermal resistance, and is more useful in setting the upper dissipation limit for cases where additional heat sinking is used.

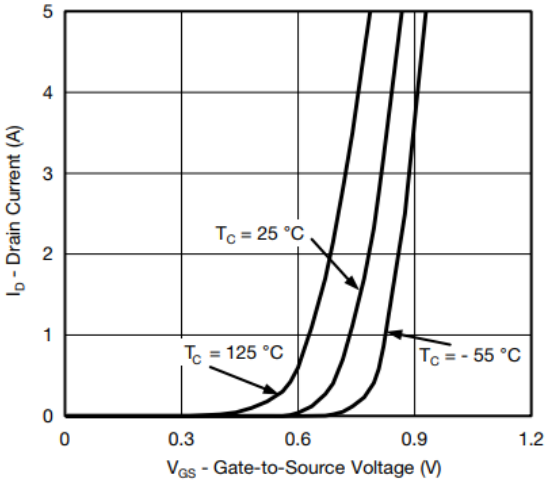
➢ Electrical Characteristics (T_A=25℃ unless otherwise noted)

Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	V_{GS} = 0V, I_D = 250μA	20			V
Gate Threshold Voltage	$V_{GS(th)}$	V_{DS} = V_{GS}, I_D = 250uA	0.5	0.7	1	V
Drain-Source On-Resistance	$R_{DS(on)}$	V_{GS} = 10V, I_D = 4.5A		14	18	mΩ
		V_{GS} = 4.5V, I_D = 3.5A		16	21	
		V_{GS} = 2.5V, I_D = 2.5A		20	27	
Zero Gate Voltage Drain Current	I_{DSS}	V_{DS} = 16V, V_{GS} = 0V			1	μA
Gate-Source Leak Current	I_{GSS}	V_{GS} = ±12V, V_{DS} = 0V			±100	nA
Transconductance	G_{FS}	V_{DS} = 5V, I_D = 4.5A		8		s
Forward Voltage	V_{SD}	V_{GS} = 0V, I_S = 0.5A			1.3	V
Input Capacitance	C_{ISS}	V_{DS} = 8V, V_{GS} = 0V, f = 1MHz		600		pF
Output Capacitance	C_{OSS}			330		
Reverse Transfer Capacitance	C_{RSS}			140		
Turn-on Delay Time	$T_{D(ON)}$	V_{GEN} = 4.5V, R_L = 10Ω V_{DS} = 10V, R_G = 6Ω I_D = 1A		7		ns
Rise Time	T_r			13		
Turn-off Delay Time	$T_{D(OFF)}$			48		
Fall Time	T_f			22		
Total Gate Charge	Q_G	V_{GS} = 4.5V, V_{DS} = 10V, I_D = 4A		8.5		nC
Gate to Source Charge	Q_{GS}			1.8		
Gate to Drain Charge	Q_{GD}			2.2		

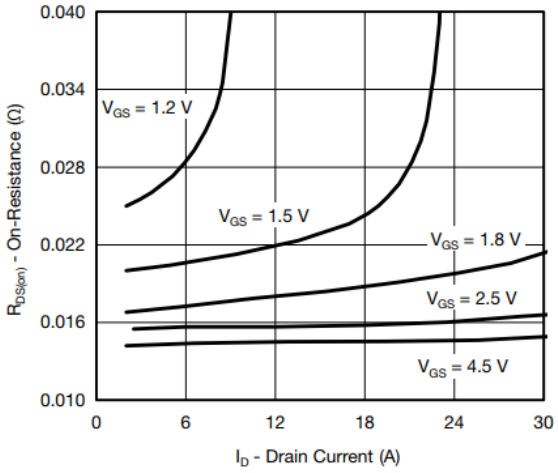
➢ Typical Performance Characteristics (T_A=25℃ unless otherwise noted)



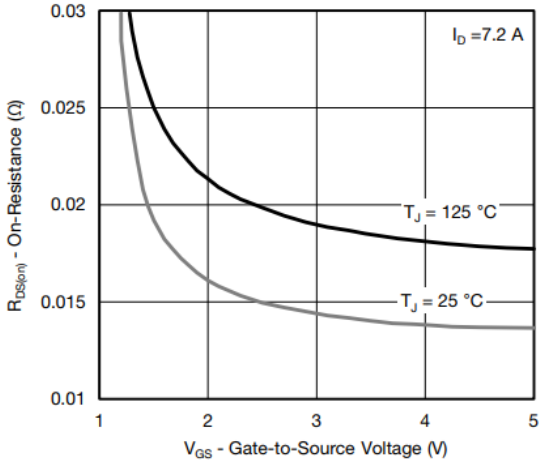
Output Characteristics



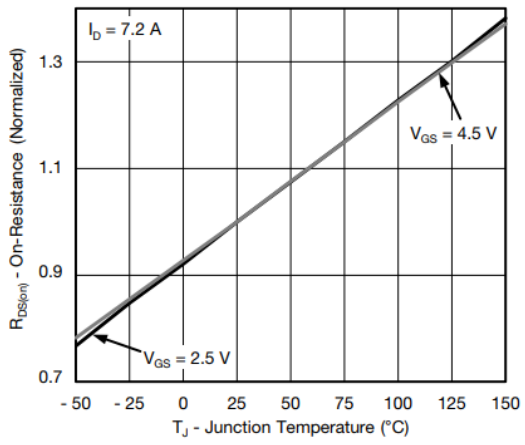
Transfer Characteristics



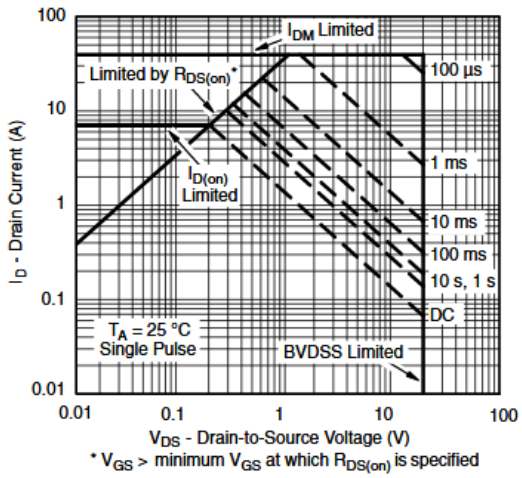
On-Resistance vs. Drain Current and Gate Voltage



On-Resistance vs. Gate-to-Source Voltage



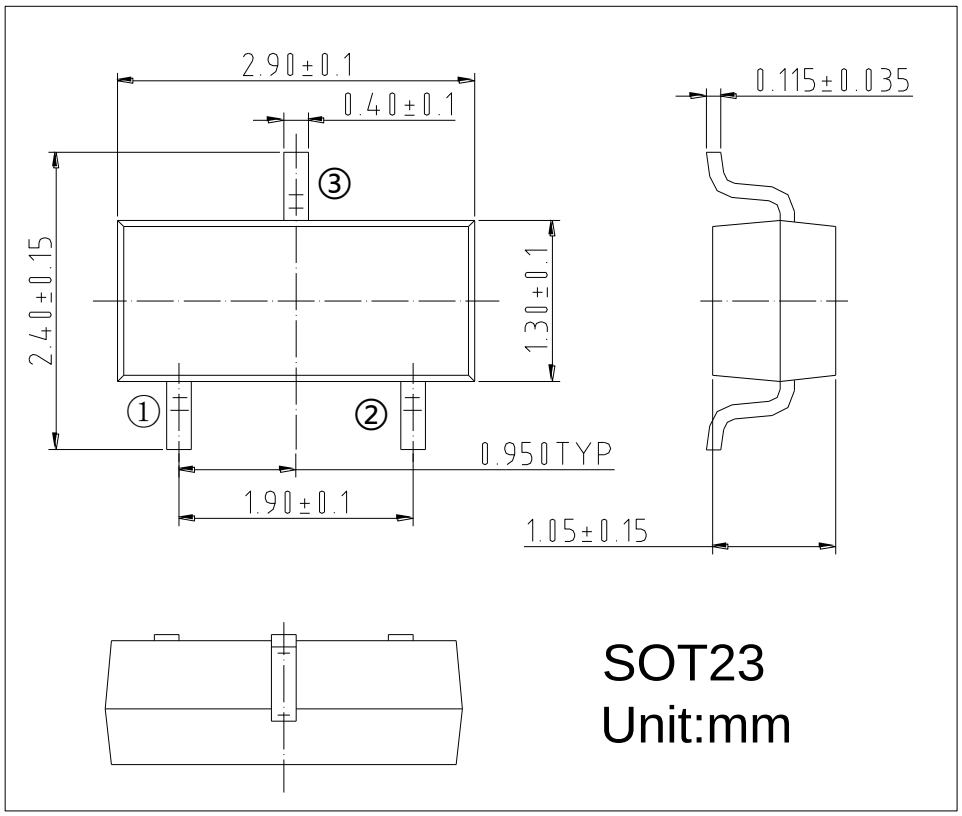
On-Resistance vs. Junction Temperature



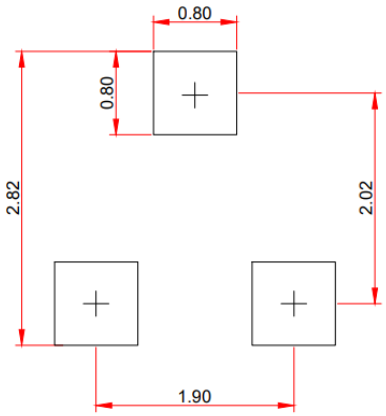
* V_{GS} > minimum V_{GS} at which $R_{DS(on)}$ is specified

Safe Operating Area, Junction-to-Ambient

➢ Package Information



Recommended Pad outline (Unit: mm)



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