



SSC8L420GN6

N-Channel Enhancement Mode MOSFET

➤ Features

V _{DS}	V _{GS}	R _{DS(ON)} Typ.	I _D
40V	±20V	2.5mΩ@10V	110A
		3mΩ@4V5	

➤ Description

This device is N-Channel enhancement MOSFET. Uses SGT technology and design to provide excellent RDSON with low gate charge. This device is suitable for use in DC-DC conversion, power switch and charging circuit.

100% UIS + ΔVDS + Rg Tested!

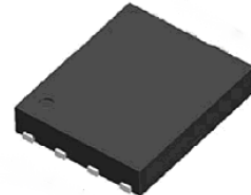
➤ Applications

- DC/DC Converters
- Power Supplies
- Motor Drive Control
- Synchronous Rectification

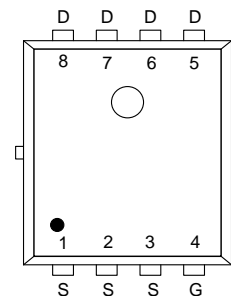
➤ Ordering Information

Device	Package	Shipping
SSC8L420GN6	PDFN5X6-8L	5000/Reel

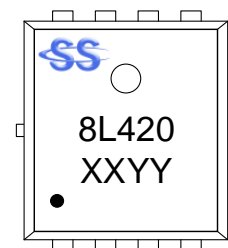
➤ Pin configuration



PDFN5X6-8L



Pin Configuration (Top View)



Marking

(XX: Product Year / YY: Product Week)



➤ Absolute Maximum Ratings ($T_A=25^{\circ}\text{C}$ unless otherwise noted)

Symbol	Parameter	Ratings	Unit
V_{DSS}	Drain-to-Source Voltage	40	V
V_{GSS}	Gate-to-Source Voltage	± 20	V
I_D	Continuous Drain Current ^d	$T_C=25^{\circ}\text{C}$	110
		$T_C=100^{\circ}\text{C}$	55
I_{DSM}	Continuous Drain Current ^a	$T_A=25^{\circ}\text{C}$	36
		$T_A=70^{\circ}\text{C}$	25
I_{DM}	Pulsed Drain Current ^b	440	A
P_D	Power Dissipation ^c	$T_C=25^{\circ}\text{C}$	50
		$T_C=100^{\circ}\text{C}$	20
P_{DSM}	Power Dissipation ^a	$T_A=25^{\circ}\text{C}$	5.2
		$T_A=70^{\circ}\text{C}$	3.3
I_{AS}	Avalanche Current ^b $L=0.5\text{mH}$ Single Pulse	37	A
E_{AS}	Avalanche Energy ^b $L=0.5\text{mH}$ Single Pulse	342	mJ
T_J	Operation junction temperature	-55~150	$^{\circ}\text{C}$
T_{STG}	Storage temperature range	-55~150	

➤ Thermal Resistance Ratings ($T_A=25^{\circ}\text{C}$ unless otherwise noted)

Symbol	Parameter	Ratings	Unit
$R_{\theta JA}$	Junction-to-Ambient Thermal Resistance ^a	24	$^{\circ}\text{C}/\text{W}$
$R_{\theta JC}$	Junction-to-Case Thermal Resistance	2.5	

Note:

- The value of $R_{\theta JA}$ is measured with the device mounted on 1 in² FR-4 board with 2oz.copper, in a still air environment with $T_A=25^{\circ}\text{C}$. The value in any given application depends on the user is specific board design. The power dissipation is based on the $t \leq 10\text{s}$ thermal resistance rating.
- Repetitive rating, pulse width limited by junction temperature.
- The power dissipation P_D is based on $T_{J(MAX)}=150^{\circ}\text{C}$, using junction-to-case thermal resistance, and is more useful in setting the upper dissipation limit for cases where additional heat sinking is used.
- The maximum current rating is package limited.

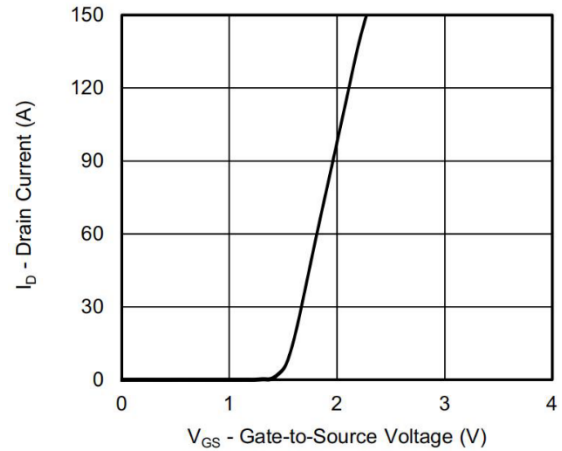
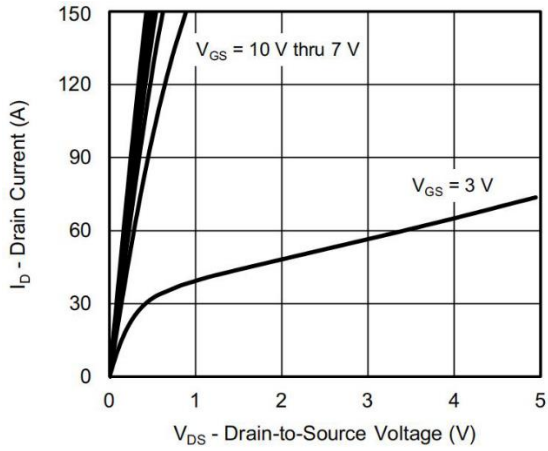


➤ **Electrical Characteristics (T_A=25°C unless otherwise noted)**

Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
Drain-Source Breakdown Voltage	V _{(BR)DSS}	V _{GS} = 0V, I _D = 250μA	40			V
Gate Threshold Voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = 250uA	1	1.4	2	V
Drain-Source On-Resistance	R _{DS(on)}	V _{GS} = 10V, I _D = 20A		2.5	3.3	mΩ
		V _{GS} = 4.5V, I _D = 10A		3	4	
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = 40V, V _{GS} = 0V			1	μA
Gate-Source Leak Current	I _{GSS}	V _{GS} = ±20V, V _{DS} = 0V			±150	nA
Transconductance	G _{FS}	V _{DS} = 5V, I _D = 20A		43		s
Forward Voltage	V _{SD}	V _{GS} = 0V, I _S = 10A		0.73	1.3	V
Gate Resistance	R _G	V _{DS} = 0V, f = 1MHz		0.75		Ω
Input Capacitance	C _{ISS}	V _{DS} = 20V, V _{GS} = 0V, f = 1MHz		2747		pF
Output Capacitance	C _{OSS}			680		
Reverse Transfer Capacitance	C _{RSS}			62		
Total Gate Charge	Q _G	V _{GS} = 10V, V _{DS} = 20V, I _D = 20A		57		nC
Gate to Source Charge	Q _{GS}			14		
Gate to Drain Charge	Q _{GD}			6		
Turn-on Delay Time	T _{D(ON)}	V _{GS} = 10V, V _{DS} = 20V, R _L = 1Ω, R _G = 3Ω		10		ns
Rise Time	T _r			3		
Turn-off Delay Time	T _{D(OFF)}			34		
Fall Time	T _f			2.9		
Diode Recovery Time	T _{rr}	I _F =20A, di/dt=500A/us		14		ns
Diode Recovery Charge	Q _{rr}	I _F =20A, di/dt=500A/us		43		nC

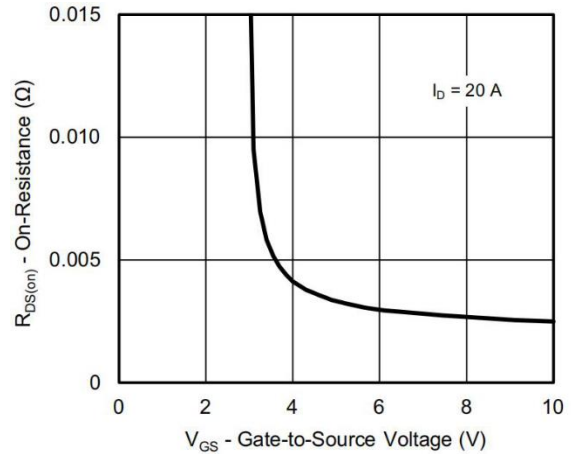
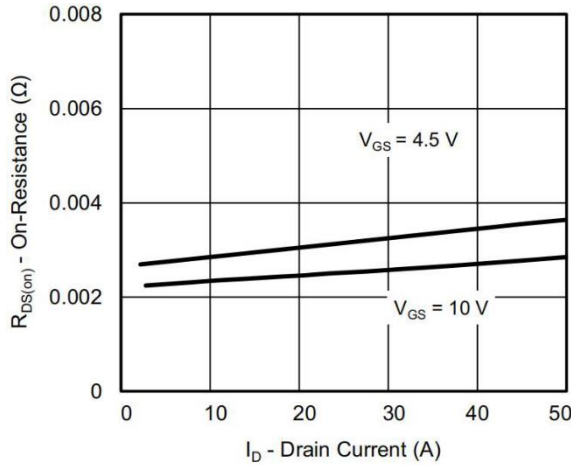


➤ **Typical Performance Characteristics (T_A=25°C unless otherwise noted)**



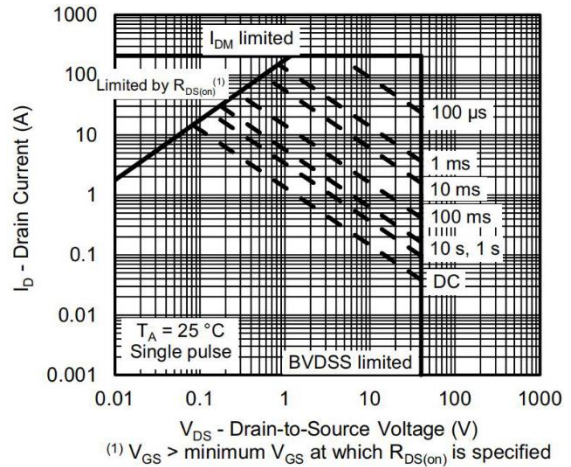
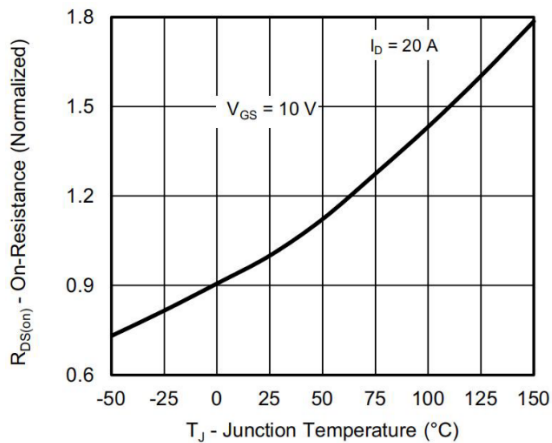
Output Characteristics

Transfer Characteristics



On-Resistance vs. Drain Current

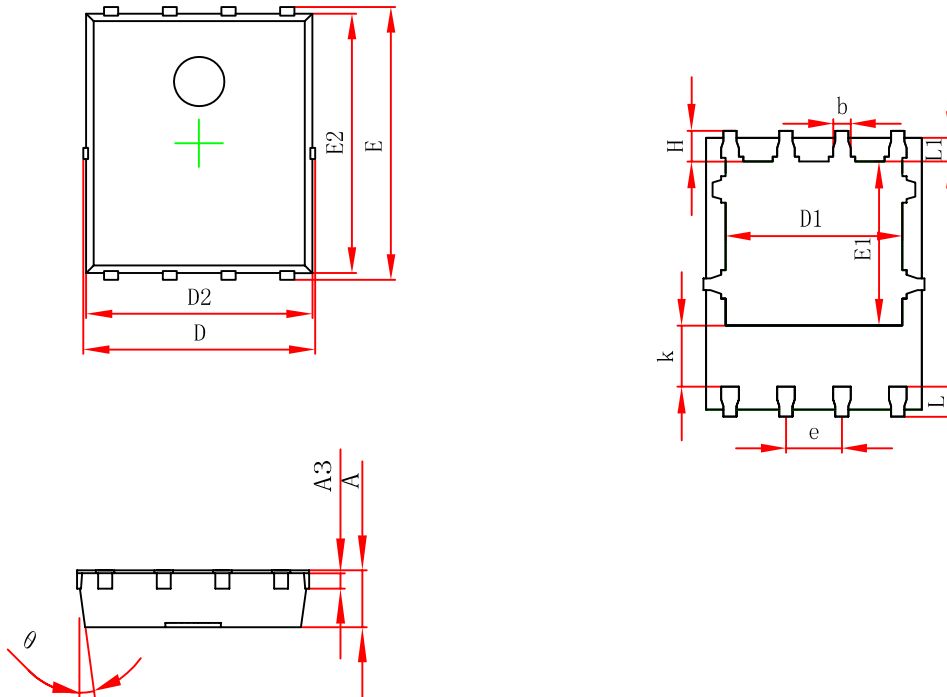
On-Resistance vs. Gate-to-Source Voltage



On-Resistance vs. Junction Temperature

Safe Operating Area

Package Information

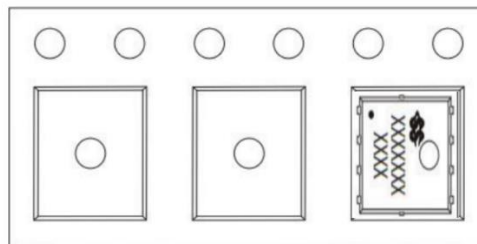
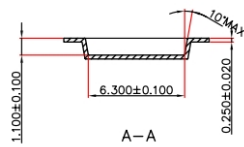
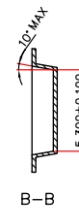
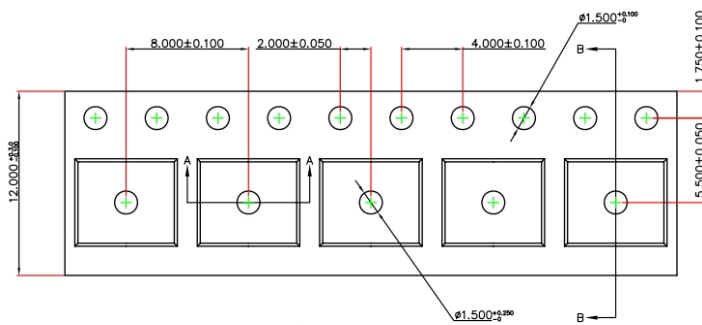
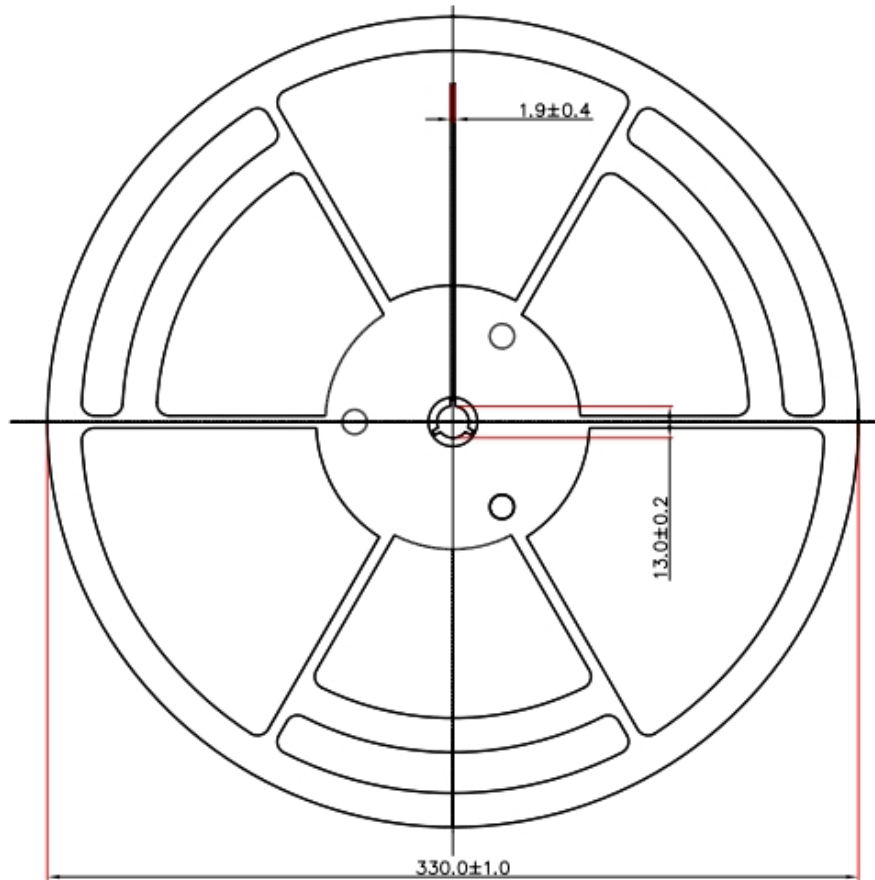


Package: PDNF5X6-8L

Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	0.900	1.000	0.035	0.039
A3	0.254REF.		0.010REF.	
D	4.944	5.096	0.195	0.201
E	5.974	6.126	0.235	0.241
D1	3.910	4.110	0.154	0.162
E1	3.375	3.575	0.133	0.141
D2	4.824	4.976	0.190	0.196
E2	5.674	5.826	0.223	0.229
k	1.190	1.390	0.047	0.055
b	0.350	0.450	0.014	0.018
e	1.270TYP.		0.050TYP.	
L	0.559	0.711	0.022	0.028
L1	0.424	0.576	0.017	0.023
H	0.574	0.726	0.023	0.029
θ	10°	12°	10°	12°



➤ Tape and Reel





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