



## SSC8LA14GT6

### N-Channel Enhancement Mode MOSFET

#### ➤ Features

V <sub>DS</sub>	V <sub>GS</sub>	R <sub>DS(ON)</sub>	I <sub>D</sub>
100V	±20V	1.6mΩ@10V	300A
		2mΩ@6V0	

#### ➤ Description

This device is N-Channel enhancement MOSFET. Uses SGT technology and design to provide excellent R<sub>DS(ON)</sub> with low gate charge. This device is suitable for use in DC-DC conversion, power switch and charging circuit.

**100% UIS + ΔVDS + R<sub>g</sub> Tested!**

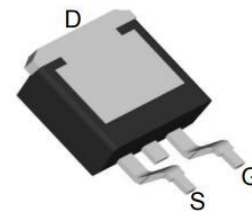
#### ➤ Applications

- Motor Drive Control
- Portable Devices
- DCDC Conversion
- Power Supplies
- Synchronous Rectification

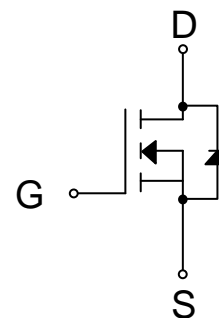
#### ➤ Ordering Information

Device	Package	Shipping
SSC8LA14GT6	TO-263-3L	1000/Box

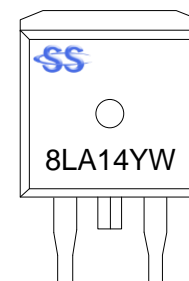
#### ➤ Pin Configuration



**TO-263-3L (Bottom View)**



**Pin Configuration**



**Marking**

(YW: Internal Traceability Code)



## ➤ Absolute Maximum Ratings ( $T_A=25^{\circ}\text{C}$ unless otherwise noted)

Symbol	Parameter	Ratings	Unit
$V_{DSS}$	Drain-to-Source Voltage	100	V
$V_{GSS}$	Gate-to-Source Voltage	$\pm 20$	V
$I_D$	Continuous Drain Current <sup>d</sup>	$T_C=25^{\circ}\text{C}$	300
		$T_C=100^{\circ}\text{C}$	180
$I_{DSM}$	Continuous Drain Current <sup>a</sup>	$T_A=25^{\circ}\text{C}$	57
		$T_A=70^{\circ}\text{C}$	40
$I_{DM}$	Pulsed Drain Current <sup>b</sup>	1200	A
$P_D$	Power Dissipation <sup>c</sup>	$T_C=25^{\circ}\text{C}$	300
		$T_C=100^{\circ}\text{C}$	120
$P_{DSM}$	Power Dissipation <sup>a</sup>	$T_A=25^{\circ}\text{C}$	8
		$T_A=70^{\circ}\text{C}$	5
$I_{AS}$	Avalanche Current <sup>b</sup> L=0.5mH Single Pulse	80	A
$E_{AS}$	Avalanche Energy <sup>b</sup> L=0.5mH Single Pulse	1600	mJ
$T_J$	Operation junction temperature	-55~150	$^{\circ}\text{C}$
$T_{STG}$	Storage temperature range	-55~150	

## ➤ Thermal Resistance Ratings ( $T_A=25^{\circ}\text{C}$ unless otherwise noted)

Symbol	Parameter	Ratings	Unit
$R_{\theta JA}$	Junction-to-Ambient Thermal Resistance <sup>a</sup>	15	$^{\circ}\text{C}/\text{W}$
$R_{\theta JC}$	Junction-to-Case Thermal Resistance	0.41	

Note:

- The value of  $R_{\theta JA}$  is measured with the device mounted on 1 in<sup>2</sup> FR-4 board with 2oz.copper, in a still air environment with  $T_A=25^{\circ}\text{C}$ . The value in any given application depends on the user is specific board design. The power dissipation is based on the  $t \leq 10\text{s}$  thermal resistance rating.
- Repetitive rating, pulse width limited by junction temperature.
- The power dissipation  $P_D$  is based on  $T_{J(MAX)}=150^{\circ}\text{C}$ , using junction-to-case thermal resistance, and is more useful in setting the upper dissipation limit for cases where additional heat sinking is used.
- The maximum current rating is package limited.

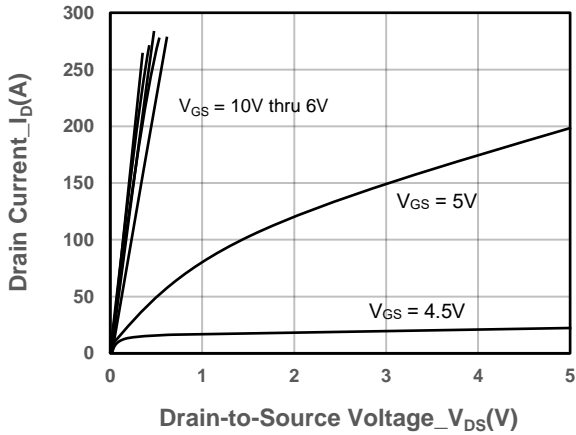


➤ **Electrical Characteristics (T<sub>A</sub>=25°C unless otherwise noted)**

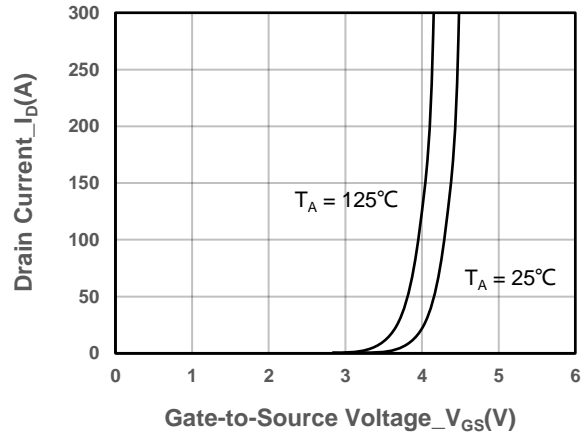
Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
Drain-Source Breakdown Voltage	V <sub>(BR)DSS</sub>	V <sub>GS</sub> = 0V, I <sub>D</sub> = 250μA	100			V
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 250uA	2	3	4	V
Drain-Source On-Resistance	R <sub>DS(on)</sub>	V <sub>GS</sub> = 10V, I <sub>D</sub> = 80A		1.6	1.9	mΩ
		V <sub>GS</sub> = 6V, I <sub>D</sub> = 50A		2	2.5	mΩ
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> = 100V, V <sub>GS</sub> = 0V			1	μA
Gate-Source Leak Current	I <sub>GSS</sub>	V <sub>GS</sub> = ±20V, V <sub>DS</sub> = 0V			±100	nA
Gate Resistance	R <sub>G</sub>	V <sub>DS</sub> = 0V, f = 1MHz		2.6		mΩ
Forward Voltage	V <sub>SD</sub>	V <sub>GS</sub> = 0V, I <sub>S</sub> = 100A		0.87	1.3	V
Input Capacitance	C <sub>ISS</sub>	V <sub>DS</sub> = 50V, V <sub>GS</sub> = 0V, f = 1MHz		14600		pF
Output Capacitance	C <sub>OSS</sub>			1800		
Reverse Transfer Capacitance	C <sub>RSS</sub>			28		
Total Gate Charge	Q <sub>G</sub>	V <sub>GS</sub> = 10V, V <sub>DS</sub> = 50V, I <sub>D</sub> = 100A		206		nC
Gate to Source Charge	Q <sub>GS</sub>			77		
Gate to Drain Charge	Q <sub>GD</sub>			42		
Turn-on Delay Time	T <sub>D(ON)</sub>	V <sub>GS</sub> = 10V, V <sub>DS</sub> = 50V, R <sub>L</sub> = 0.5Ω, R <sub>G</sub> = 10Ω		87		ns
Rise Time	T <sub>r</sub>			110		
Turn-off Delay Time	T <sub>D(OFF)</sub>			212		
Fall Time	T <sub>f</sub>			159		
Diode Recovery Time	T <sub>rr</sub>	I <sub>F</sub> =100A, di/dt=100A/us		96		ns
Diode Recovery Charge	Q <sub>rr</sub>	I <sub>F</sub> =100A, di/dt=100A/us		240		nC



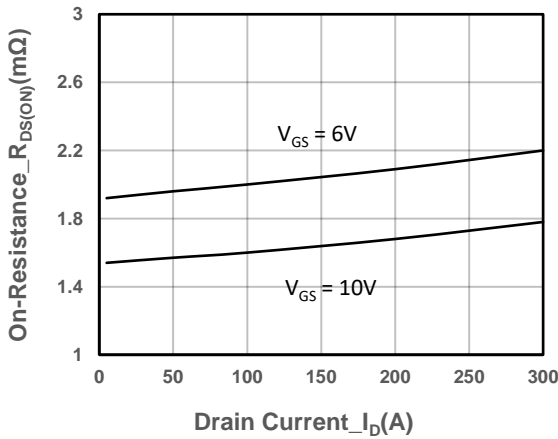
➤ **Typical Performance Characteristics ( $T_A=25^\circ\text{C}$  unless otherwise noted)**



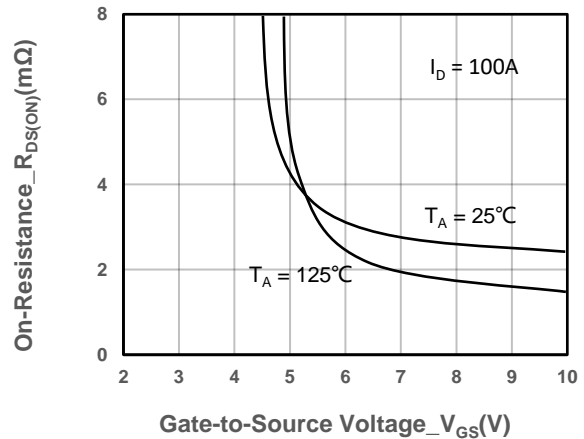
**Output Characteristics**



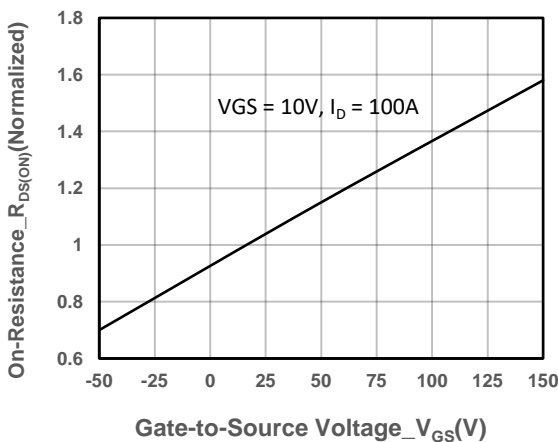
**Transfer Characteristics**



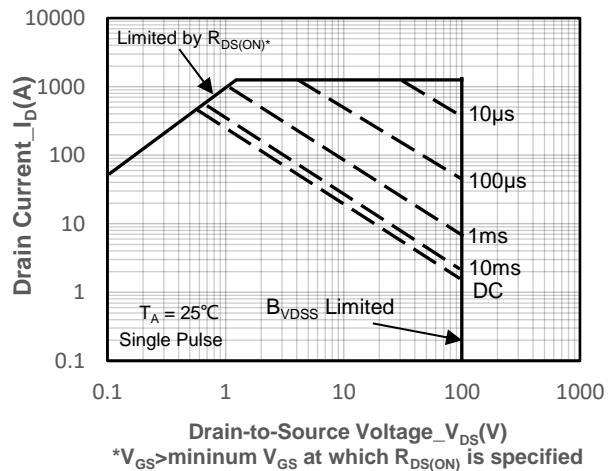
**On-Resistance vs. Drain Current and Gate Volt**



**On-Resistance vs. Gate-to-Source Voltage**

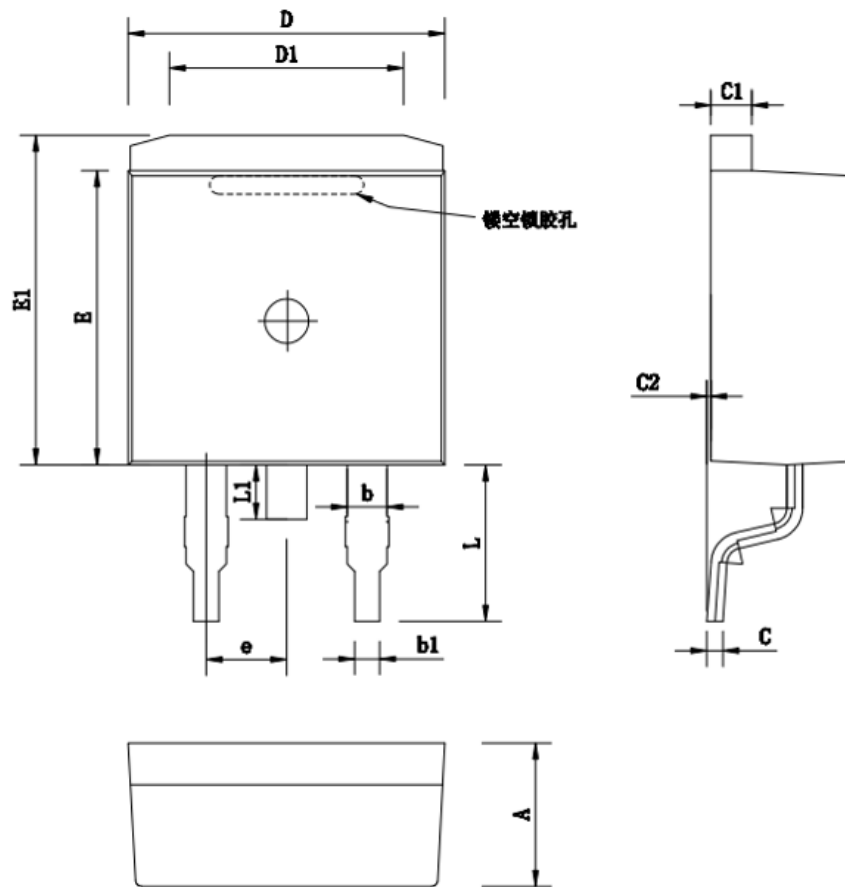


**On-Resistance vs. Junction Temperature**



**Safe Operating Area vs. Junction-to-Ambient**

## ➤ Package Information



Symbol	Dimensions In Millimeters		
	Min.	Nom.	Max.
<b>A</b>	4.40	4.50	4.60
<b>b</b>	1.20	1.28	1.36
<b>b1</b>	0.70	0.80	0.90
<b>C</b>	0.48	0.50	0.53
<b>C1</b>	1.28	1.30	1.32
<b>D</b>	9.80	10.00	10.20
<b>D1</b>	7.25	7.40	7.55
<b>E</b>	10.10	10.25	10.40
<b>E1</b>	9.10	9.20	9.30
<b>e</b>	-	2.54	-
<b>L</b>	4.70	4.90	5.10
<b>L1</b>	1.50	1.70	1.90



## DISCLAIMER

SSCSEMI RESERVES THE RIGHT TO MAKE CHANGES WITHOUT FURTHER NOTICE TO ANY PRODUCTS HEREIN TO IMPROVE RELIABILITY, FUNCTION OR DESIGN. SSCSEMI DOES NOT ASSUME ANY LIABILITY ARISING OUT OF THE APPLICATION OR USE OF ANY PRODUCT OR CIRCUIT DESCRIBED HEREIN; NEITHER DOES IT CONVEY ANY LICIENCE UNDER ITS PATENT RIGHTS, NOR THE RIGHTS OF OTHERS.

THE GRAPHS PROVIDED IN THIS DOCUMENT ARE STATISTICAL SUMMARIES BASED ON A LIMITED NUMBER OF SAMPLES AND ARE PROVIDED FOR INFORMATIONAL PURPOSE ONLY. THE PERFORMANCE CHARACTERISTICS LISTED IN THEM ARE NOT TESTED OR GUARANTEED. IN SOME GRAPHS, THE DATA PRESENTED MAY BE OUTSIDE THE SPECIFIED OPERATING RANGE (E.G. OUTSIDE SPECIFIED POWER SUPPLY RANGE) AND THEREFORE OUTSIDE THE WARRANTED RANGE.

OUR PRODUCT SPECIFICATIONS ARE ONLY VALID IF OBTAINED THROUGH THE COMPANY'S OFFICIAL WEBSITE, CRM SYSTEM, OR OUR SALES PERSONNEL CHANNELS. IF CHANGES OR SPECIAL VERSIONS ARE INVOLVED, THEY MUST BE STAMPED WITH A QUALITY SEAL AND MARKED WITH A SPECIAL VERSION NUMBER TO BE VALID.