



SSC8122GS7

N-Channel Enhancement Mode MOSFET with ESD Protection

➤ Features

VDS	VGS	RDSON Typ.	ID	ESD
20V	±8V	220mR@4V5	1.5A	2K
		300mR@2V5		
		460mR@1V8		

➤ Description

This device is a N-Channel enhancement mode MOSFET which is produced with high cell density and DMOS trench technology. This device particularly suits low voltage applications, especially for battery powered circuits, the tiny and thin outline saves PCB consumption.

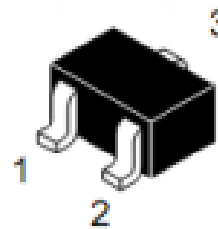
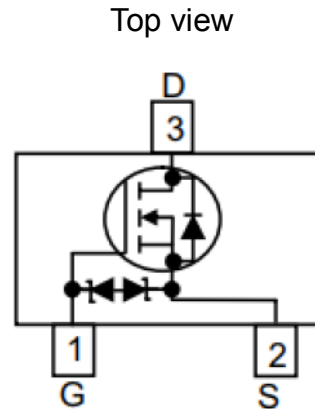
➤ Applications

- Replace Digital Transistor
- Battery Operated Systems
- Power Supply Converter Circuits
- Load/Power Switching cell Phones

➤ Ordering Information

Device	Package	Shipping
SSC8122GS7	SOT323	3000/Reel

➤ Pin configuration



SOT323



Marking



➤ **Absolute Maximum Ratings**($T_A=25^{\circ}\text{C}$ unless otherwise noted)

Symbol	Parameter	Ratings	Unit
V_{DSS}	Drain-to-Source Voltage	20	V
V_{GSS}	Gate-to-Source Voltage	± 8	V
I_D	Continuous Drain Current ^a	1.5	A
I_{DM}	Pulsed Drain Current ^b	4.5	A
P_D	Power Dissipation ^c	0.46	W
P_{DSM}	Power Dissipation ^a	0.25	W
T_J	Operation junction temperature	-55 to 150	$^{\circ}\text{C}$
T_{STG}	Storage temperature range	-55 to 150	$^{\circ}\text{C}$

➤ **Thermal Resistance Ratings**($T_A=25^{\circ}\text{C}$ unless otherwise noted)

Symbol	Parameter	Typical	Maximum	Unit
$R_{\theta JA}$	Junction-to-Ambient Thermal Resistance ^a		500	$^{\circ}\text{C}/\text{W}$
$R_{\theta JC}$	Junction-to-Case Thermal Resistance		270	

Note:

- The value of $R_{\theta JA}$ is measured with the device mounted on 1 in² FR-4 board with 2oz.copper,in a still air environment with $T_A=25^{\circ}\text{C}$.The value in any given application depends on the user is specific board design. The current rating is based on the $t \leq 10\text{s}$ thermal resistance rating.
- Repetitive rating, pulse width limited by junction temperature.
- The power dissipation P_D is based on $T_J(\text{MAX})=150^{\circ}\text{C}$, using junction-to-case thermal resistance, and is more useful in setting the upper dissipation limit for cases where additional heat sinking is used.

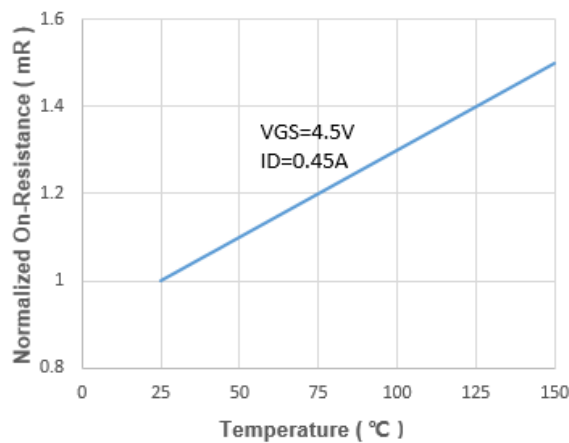
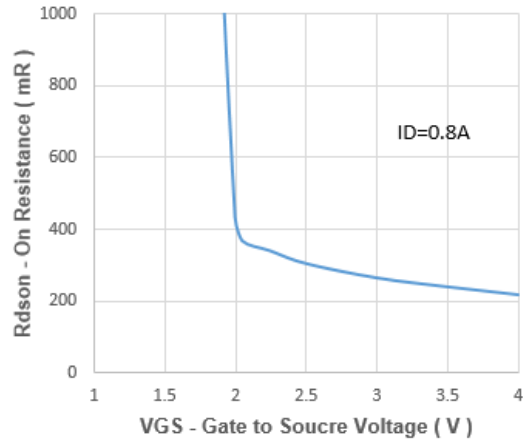
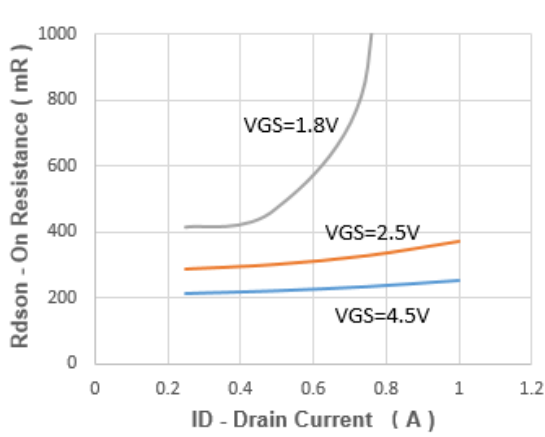
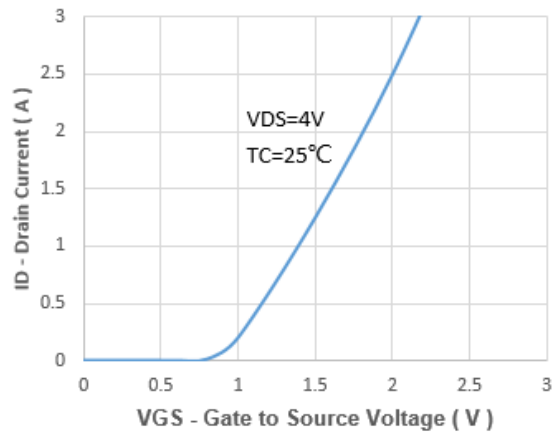
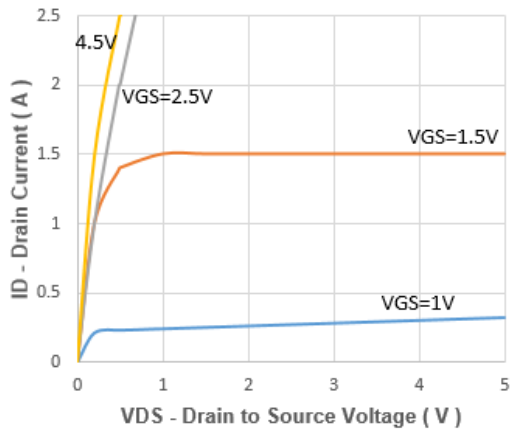


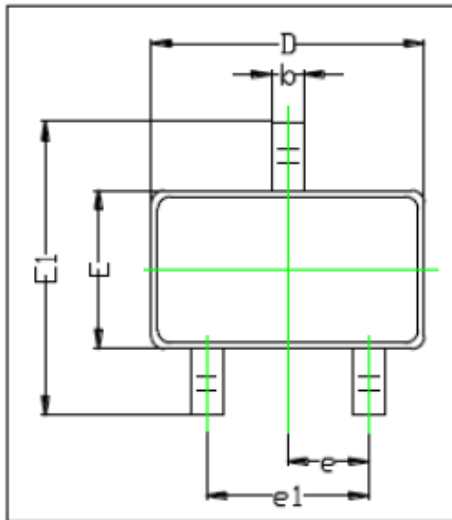
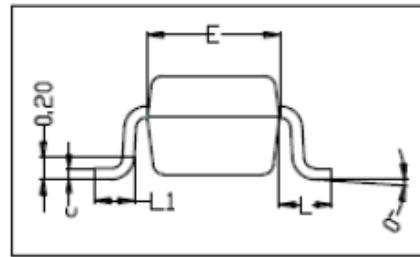
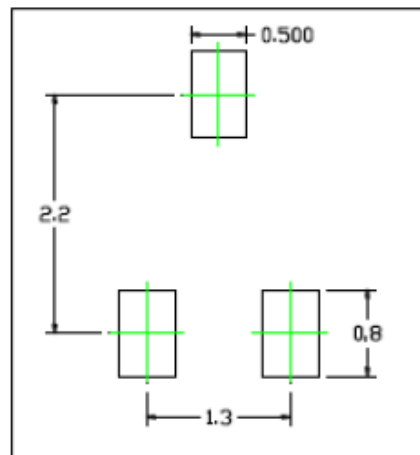
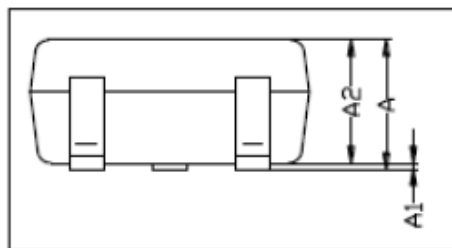
➤ **Electronics Characteristics**($T_A=25^{\circ}\text{C}$ unless otherwise noted)

Symbol	Parameter	Test Conditions	Min	Typ.	Max	Unit
$V_{(BR)DSS}$	Drain-Source Breakdown Voltage	$V_{GS}=0V, I_D=250\mu A$	20			V
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS}=V_{GS}, I_D=250\mu A$	0.5	0.8	1.2	V
$R_{DS(on)}$	Drain-Source On- Resistance	$V_{GS}=4.5V, I_D=0.55A$		220	300	mR
		$V_{GS}=2.5V, I_D=0.45A$		300	400	
		$V_{GS}=1.8V, I_D=0.35A$		460	600	
I_{DSS}	Zero Gate Voltage Drain Current	$V_{DS}=16V, V_{GS}=0V$			1	μA
I_{GSS}	Gate-Source leak current	$V_{GS}=\pm 8V, V_{DS}=0V$			± 10	μA
G_{FS}	Forward Transconductance	$V_{DS}=5V, I_D=0.45A$		1.8		S
V_{SD}	Forward Voltage	$V_{GS}=0V, I_S=0.5A$			1.3	V
C_{iss}	Input Capacitance	$V_{DS}=10V, V_{GS}=0V,$ $f=100KHZ$		56		pF
C_{oss}	Output Capacitance			15		
C_{rss}	Reverse Transfer Capacitance			9		
$T_{D(ON)}$	Turn-on delay time	$V_{GS}=4.5V,$ $V_{DD}=10V, R_G=6R,$ $I_D=0.55A$		22		ns
$T_{D(OFF)}$	Turn-off delay time			700		



➤ Typical Characteristics ($T_A=25^\circ\text{C}$ unless otherwise noted)



➤ Package Information
TOP VIEW

SIDE VIEW

SOLDERING PATTERN

FRONT VIEW


SYMBOL	DIMENSIONS IN MILLIMETER	
	MIN	MAX
A	0.900	1.000
A1	0.00	0.100
A2	0.900	1.000
b	0.200	0.400
c	0.080	0.150
D	2.000	2.200
E	1.150	1.350
E1	2.150	2.450
e	0.650 TYP.	
e1	1.200	1.400
L	0.525 REF.	
L1	0.260	0.460
θ	0°	8°



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