



## SSCL5N60GN4

### N-Channel Enhancement Mode MOSFET

#### ➤ Features

V <sub>DS</sub>	V <sub>GS</sub>	R <sub>DS(ON)</sub> Typ.	I <sub>D</sub>
60V	±20V	5 mΩ@10V	75A
		6.6mΩ@4.5V	

#### ➤ Description

This device is N-Channel enhancement MOSFET. Uses SGT technology and design to provide excellent RDSON with low gate charge. This device is suitable for use in DC-DC conversion, power switch and charging circuit.

**100% UIS + ΔVDS + Rg Tested!**

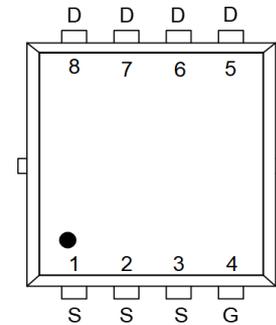
#### ➤ Applications

- Motor Drive Control
- DCDC Conversion
- Power Supplies
- Synchronous Rectification

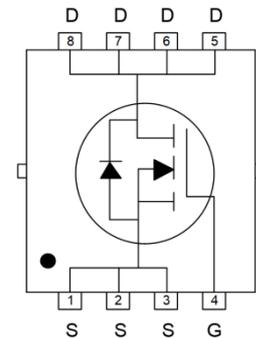
#### ➤ Ordering Information

Device	Package	Shipping
SSCL5N60GN4	PDFN3.3X3.3-8L	5000/Reel

#### ➤ Pin Configuration



**PDFN3.3X3.3-8L (Top View)**



**Pin Configuration**



**Marking**

(XXYY: Internal Traceability Code)



## ➤ Absolute Maximum Ratings ( $T_A=25^{\circ}\text{C}$ unless otherwise noted)

Symbol	Parameter	Ratings	Unit
$V_{DSS}$	Drain-to-Source Voltage	60	V
$V_{GSS}$	Gate-to-Source Voltage	$\pm 20$	V
$I_D$	Continuous Drain Current <sup>d</sup>	$T_C=25^{\circ}\text{C}$	75
		$T_C=100^{\circ}\text{C}$	47
$I_{DSM}$	Continuous Drain Current <sup>a</sup>	$T_A=25^{\circ}\text{C}$	16
		$T_A=70^{\circ}\text{C}$	13
$I_{DM}$	Pulsed Drain Current <sup>b</sup>	300	A
$P_D$	Power Dissipation <sup>c</sup>	$T_C=25^{\circ}\text{C}$	60
		$T_C=100^{\circ}\text{C}$	24
$P_{DSM}$	Power Dissipation <sup>a</sup>	$T_A=25^{\circ}\text{C}$	2.8
		$T_A=70^{\circ}\text{C}$	1.8
$I_{AS}$	Avalanche Current <sup>b</sup> L=0.5mH Single Pulse	20	A
$E_{AS}$	Avalanche Energy <sup>b</sup> L=0.5mH Single Pulse	100	mJ
$T_J$	Operation junction temperature	-55~150	$^{\circ}\text{C}$
$T_{STG}$	Storage temperature range	-55~150	

## ➤ Thermal Resistance Ratings ( $T_A=25^{\circ}\text{C}$ unless otherwise noted)

Symbol	Parameter	Ratings	Max.	Unit
$R_{\theta JA}$	Junction-to-Ambient Thermal Resistance <sup>a</sup>	45	55	$^{\circ}\text{C}/\text{W}$
$R_{\theta JC}$	Junction-to-Case Thermal Resistance	2.1	2.7	

Note:

- The value of  $R_{\theta JA}$  is measured with the device mounted on 1 in<sup>2</sup> FR-4 board with 2oz.copper, in a still air environment with  $T_A=25^{\circ}\text{C}$ . The value in any given application depends on the user is specific board design. The power dissipation is based on the  $t \leq 10\text{s}$  thermal resistance rating.
- Repetitive rating, pulse width limited by junction temperature.
- The power dissipation  $P_D$  is based on  $T_{J(MAX)}=150^{\circ}\text{C}$ , using junction-to-case thermal resistance, and is more useful in setting the upper dissipation limit for cases where additional heat sinking is used.
- The maximum current rating is package limited.

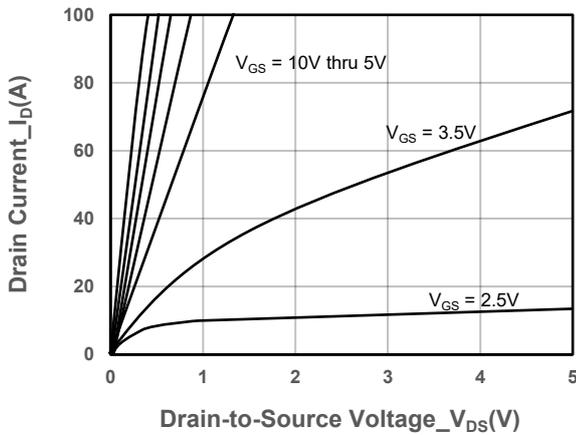


➤ **Electrical Characteristics (T<sub>A</sub>=25°C unless otherwise noted)**

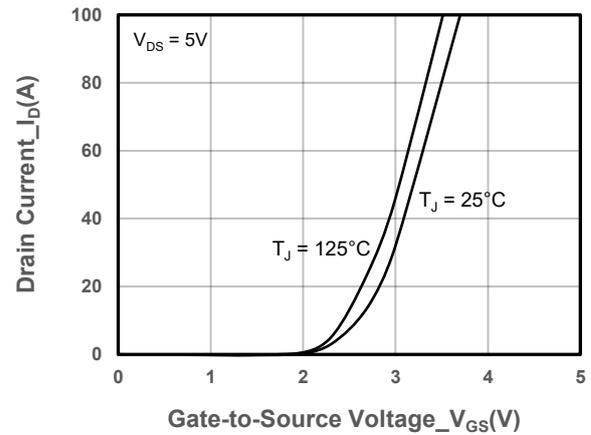
Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
Drain-Source Breakdown Voltage	V <sub>(BR)DSS</sub>	V <sub>GS</sub> = 0V, I <sub>D</sub> = 250μA	60			V
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 250uA	1.0	1.6	2.5	V
Drain-Source On-Resistance	R <sub>DS(on)</sub>	V <sub>GS</sub> = 10V, I <sub>D</sub> = 20A		5	7.5	mΩ
		V <sub>GS</sub> = 4.5V, I <sub>D</sub> = 10A		6.6	9.5	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> = 60V, V <sub>GS</sub> = 0V			1	μA
Gate-Source Leak Current	I <sub>GSS</sub>	V <sub>GS</sub> = ±20V, V <sub>DS</sub> = 0V			±100	nA
Forward Voltage	V <sub>SD</sub>	V <sub>GS</sub> = 0V, I <sub>S</sub> = 10A		0.7	1.3	V
Gate Resistance	R <sub>G</sub>	V <sub>DS</sub> = 0V, f = 1MHz		1.8		Ω
Input Capacitance	C <sub>ISS</sub>	V <sub>DS</sub> = 30V, V <sub>GS</sub> = 0V, f = 1MHz		2060		pF
Output Capacitance	C <sub>OSS</sub>			690		
Reverse Transfer Capacitance	C <sub>RSS</sub>			34		
Total Gate Charge	Q <sub>G</sub>	V <sub>GS</sub> = 10V, V <sub>DS</sub> = 30V, I <sub>D</sub> = 20A		33		nC
Gate to Source Charge	Q <sub>GS</sub>			11		
Gate to Drain Charge	Q <sub>GD</sub>			8		
Turn-on Delay Time	T <sub>D(ON)</sub>	V <sub>GS</sub> = 10V, V <sub>DS</sub> = 30V, I <sub>D</sub> = 20A, R <sub>G</sub> = 3Ω		15		ns
Rise Time	T <sub>r</sub>			35		
Turn-off Delay Time	T <sub>D(OFF)</sub>			31		
Fall Time	T <sub>f</sub>			28		
Diode Recovery Time	T <sub>rr</sub>	I <sub>F</sub> =20A, di/dt=100A/us		45		ns
Diode Recovery Charge	Q <sub>rr</sub>	I <sub>F</sub> =20A, di/dt=100A/us		32		nC



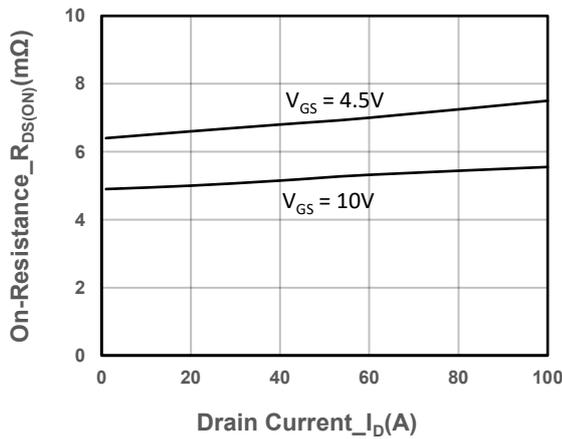
➤ **Typical Performance Characteristics (T<sub>A</sub>=25°C unless otherwise noted)**



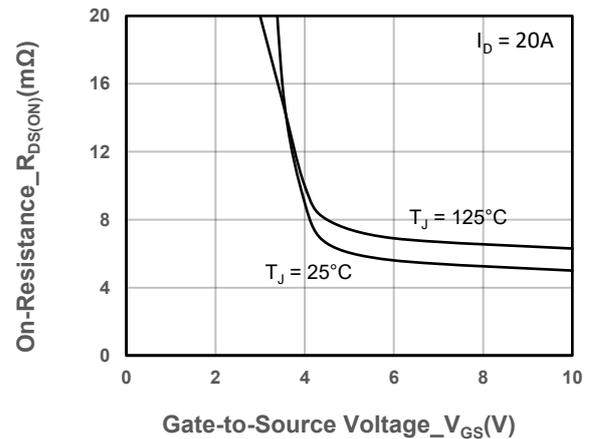
**Output Characteristics**



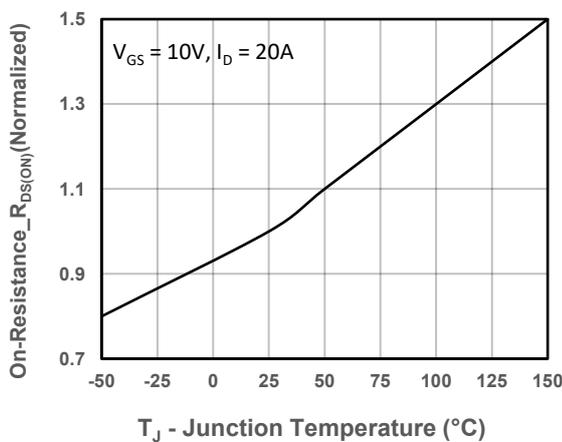
**Transfer Characteristics**



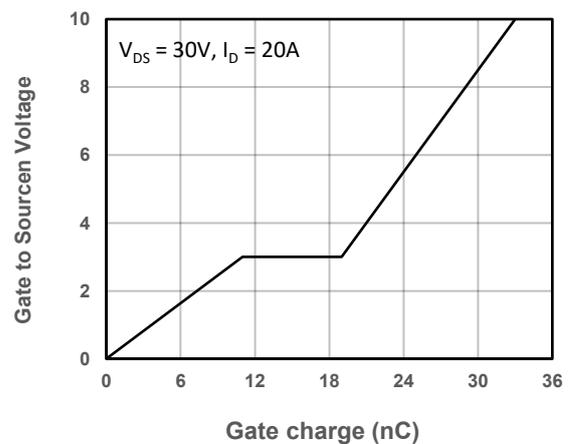
**On-Resistance vs. Drain Current and Gate Voltage**



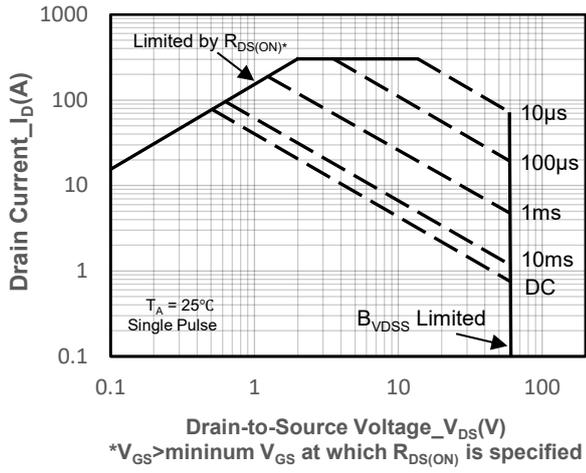
**On-Resistance vs. Gate-to-Source Voltage**



**On-Resistance vs. Junction Temperature**

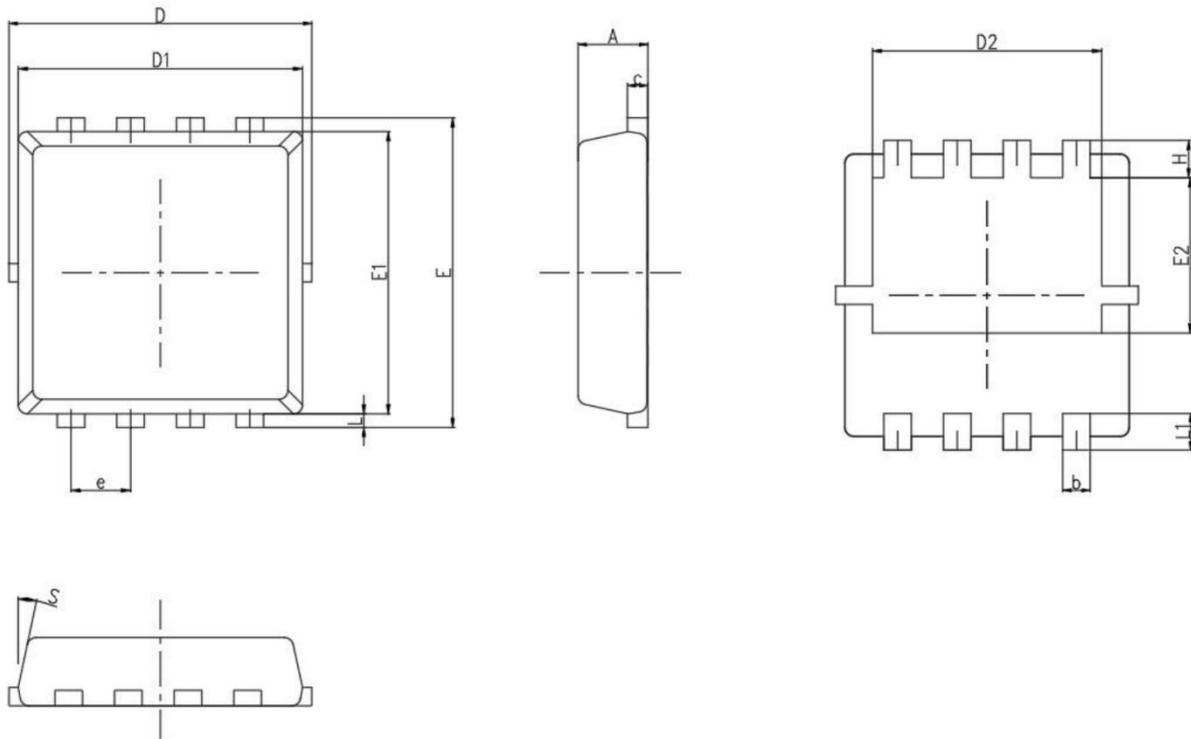


**Gate-Source Voltage vs. Gate charge**



### Safe Operating Area vs. Junction-to-Ambient

## ➤ Package Information



Symbol	MILL IMETER		
	Min	Nom	Max
A	0.65	0.75	0.9
b	0.20	0.3	0.40
c	0.1	/	0.22
D	3.1	3.3	3.45
D1	3	3.15	3.2
D2	2.55	2.5	2.75
E	3.15	3.3	3.45
E1	2.9	3.05	3.2
E2	1.55	1.75	1.95
e	0.65BSC		
L	0.06	0.15	0.2
L1	0.25	0.4	0.55
H	0.31	0.35	0.6
S	10°	12°	14°



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